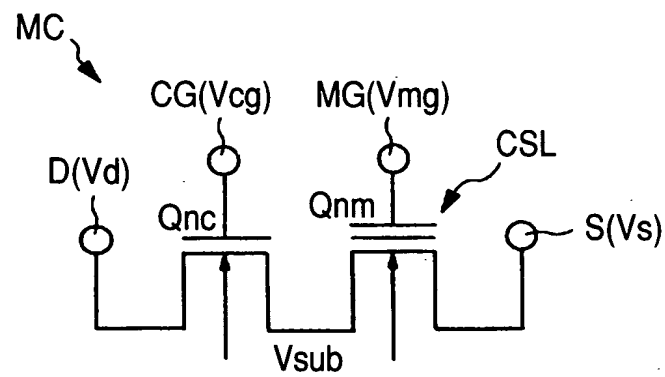
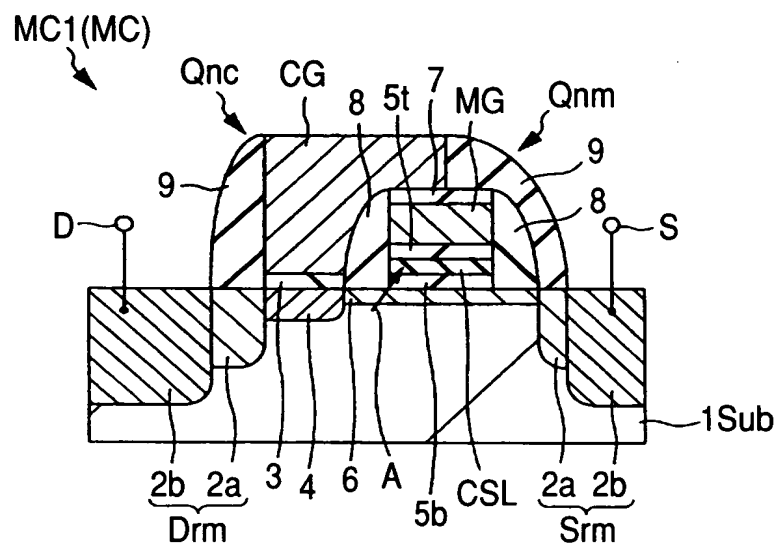


**FIG. 1**

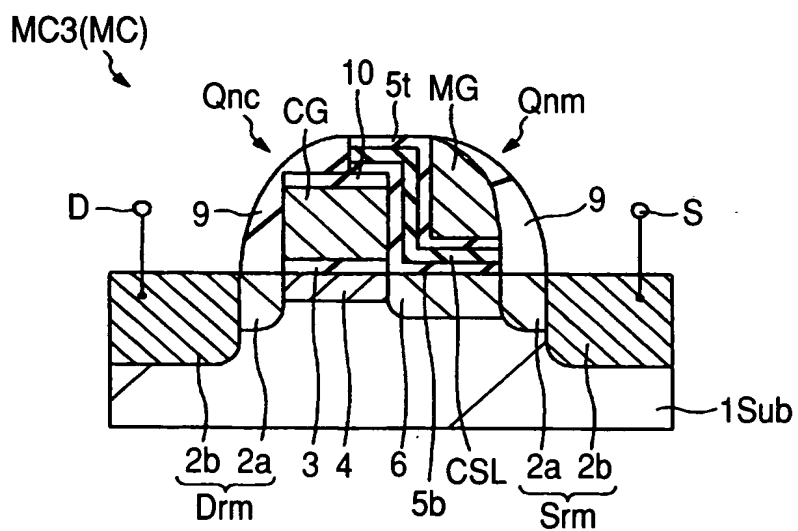


**FIG. 2**

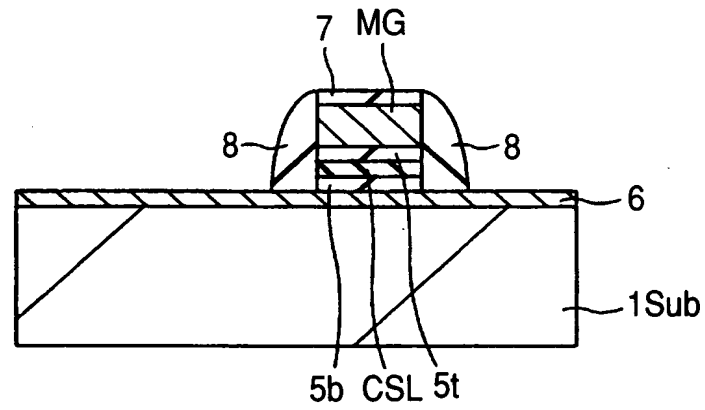


**FIG. 3**

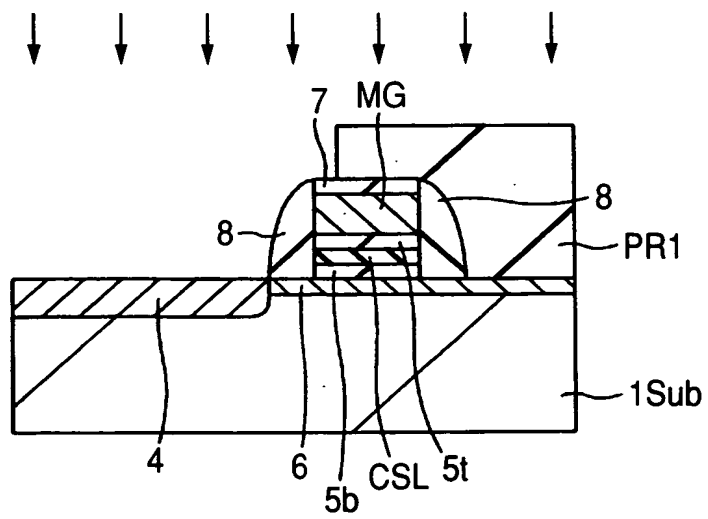
	Vd(V)	Vcg(V)	Vmg(V)	Vs(V)	Vsub(V)
Re	1	1.5	0	0	0
Er	0	1.5	14	0	0
Wr	0	1.5	12	6	0



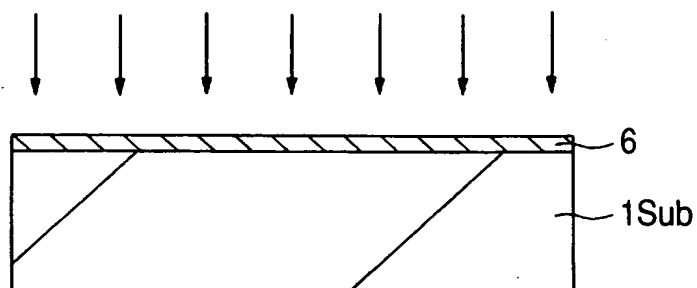
**FIG. 6**



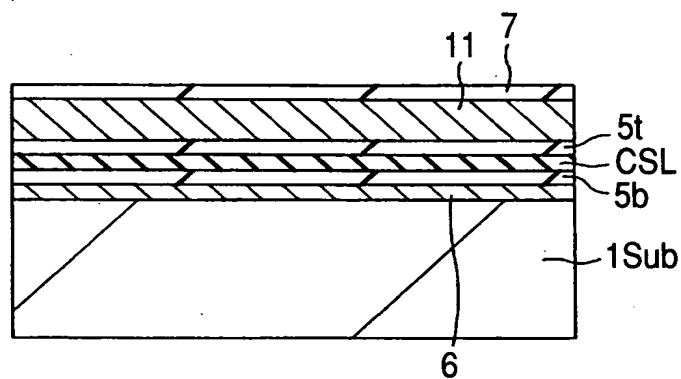
**FIG. 7**



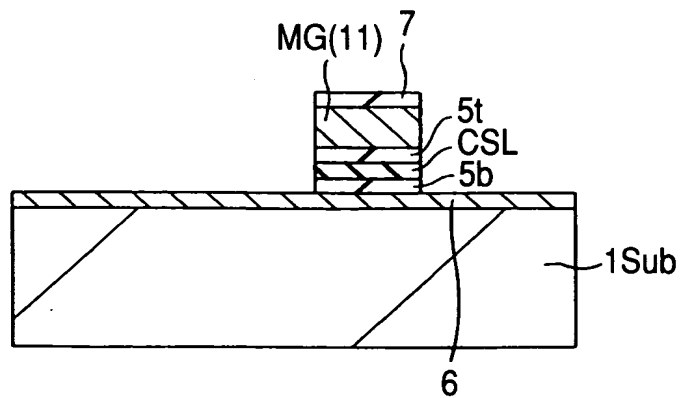
**FIG. 8**

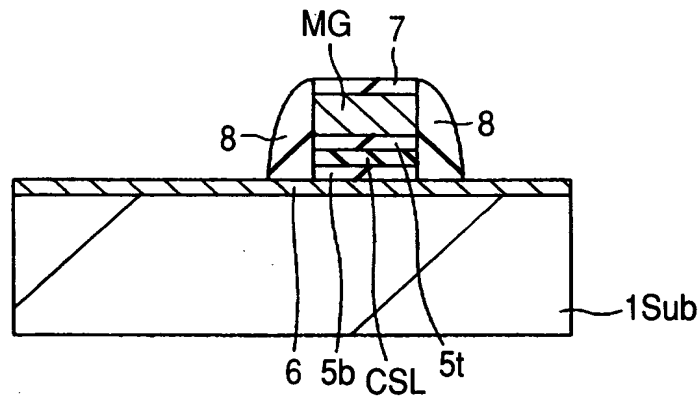
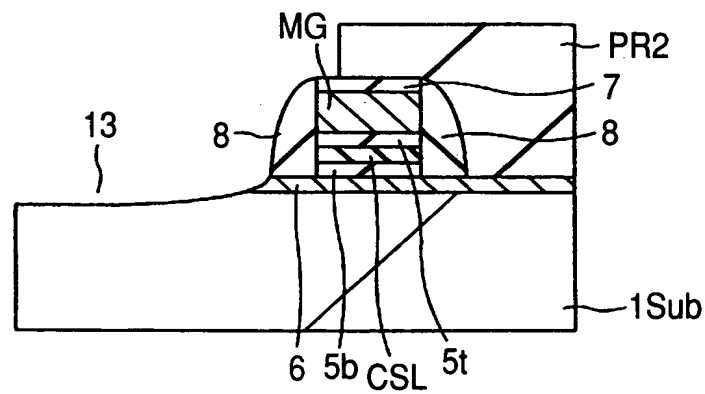


**FIG. 9**



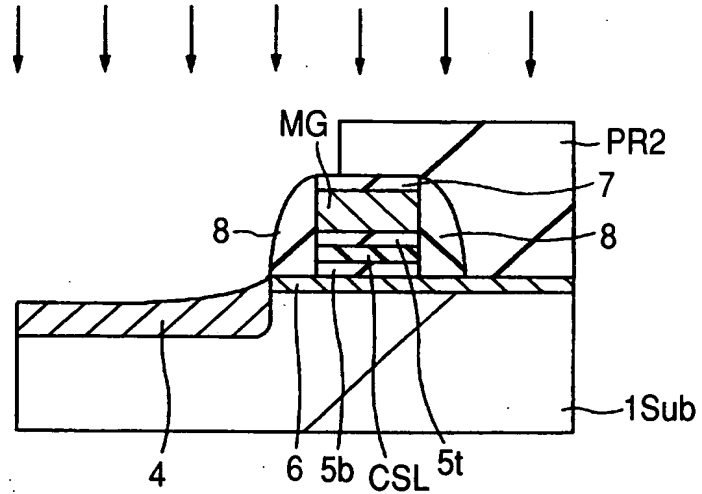
**FIG. 10**



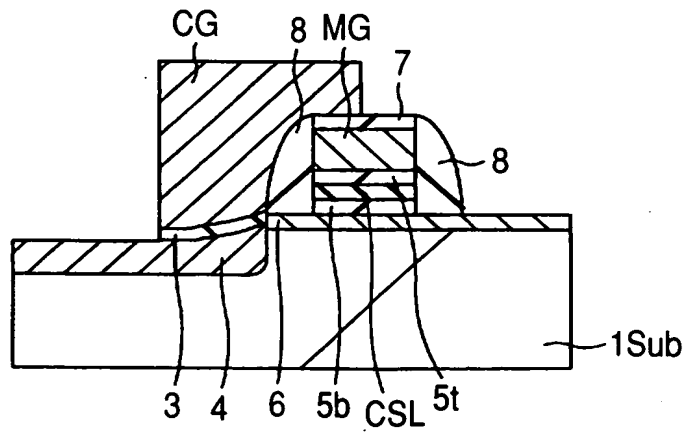
**FIG. 11****FIG. 12**

1Sub: SEMICONDUCTOR SUBSTRATE  
 6: n-TYPE SEMICONDUCTOR REGION  
 8: SIDE WALLS  
 CSL: CHARGE STORAGE LAYER  
 MG: MEMORY GATE ELECTRODE  
 13: DENT

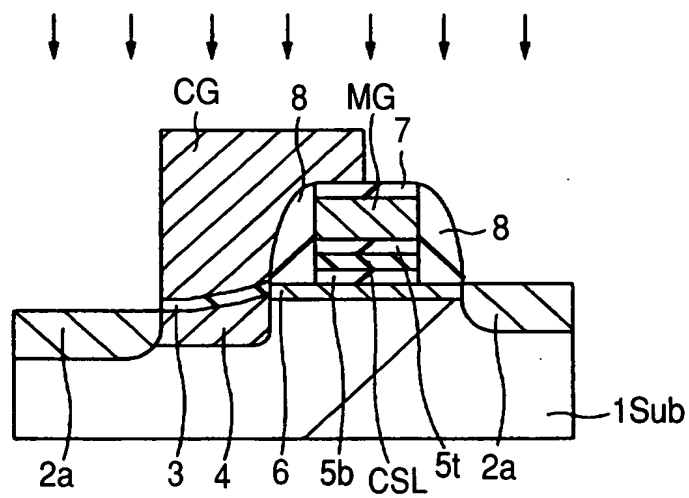
**FIG. 13**



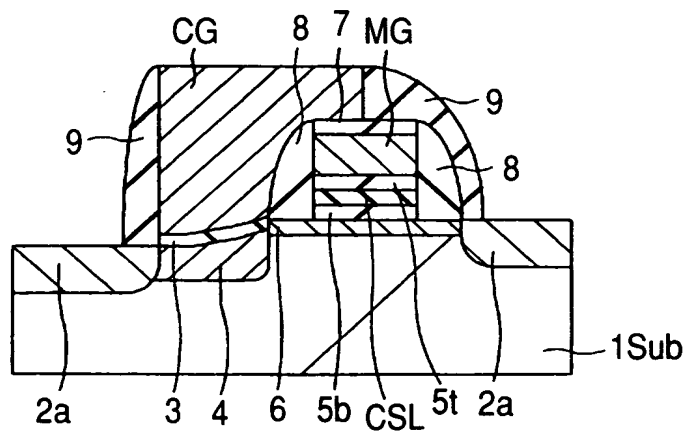
**FIG. 14**



**FIG. 15**

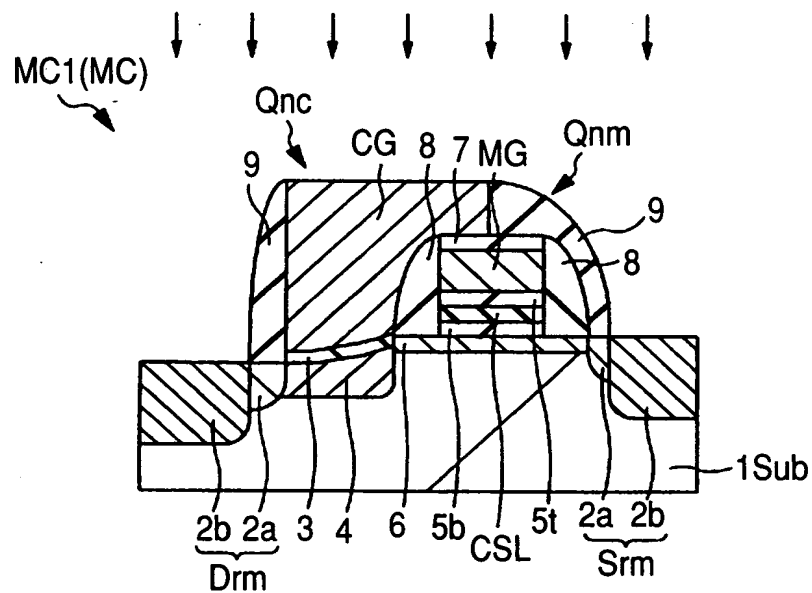


**FIG. 16**

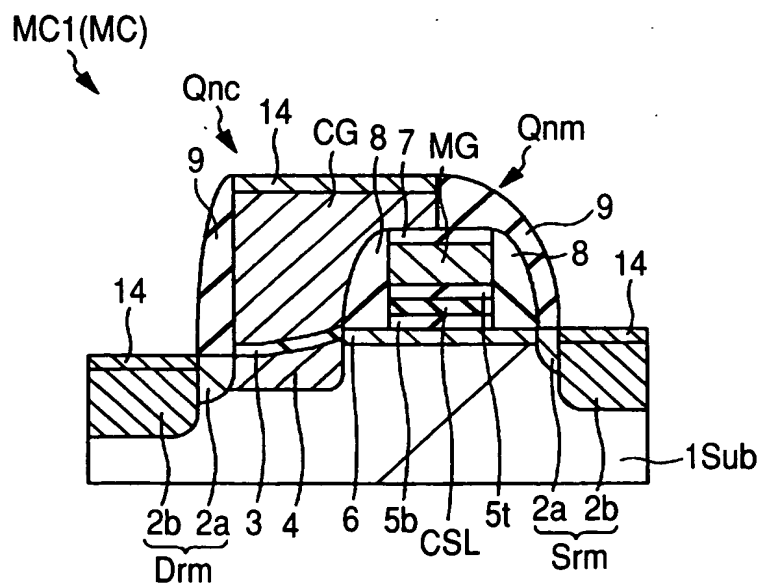




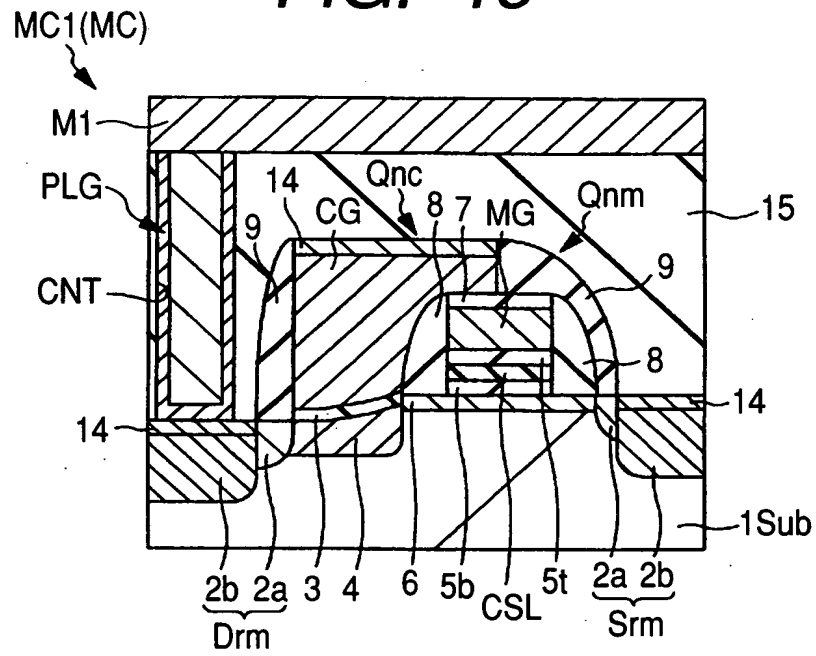
**FIG. 17**



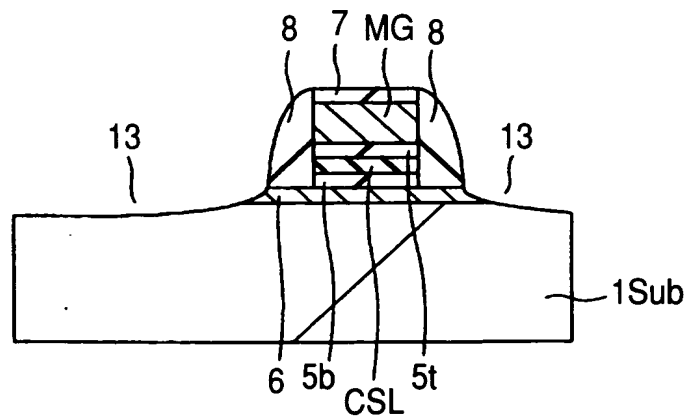
**FIG. 18**



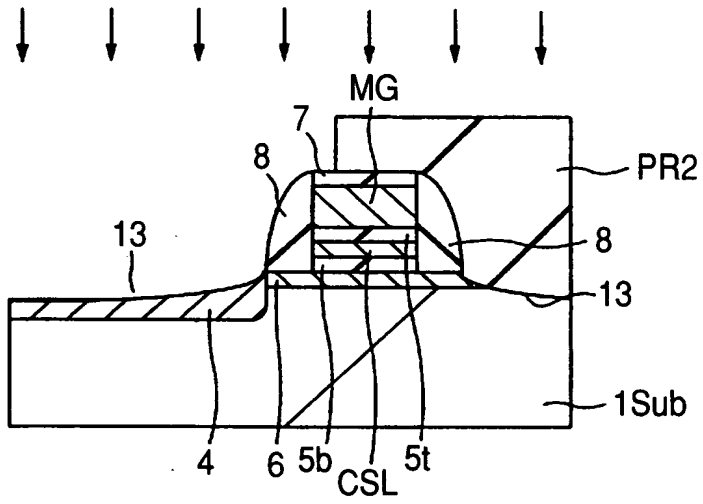
**FIG. 19**



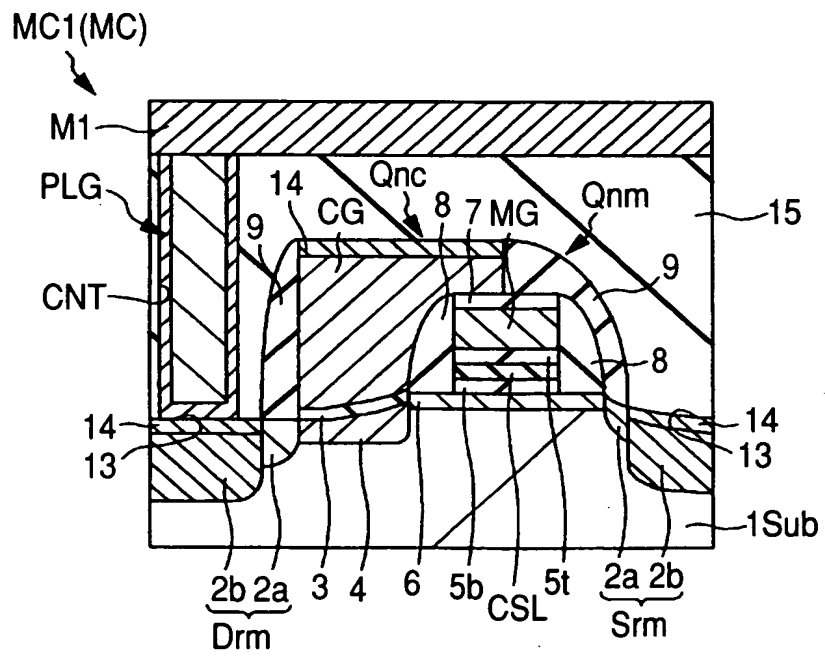
**FIG. 20**



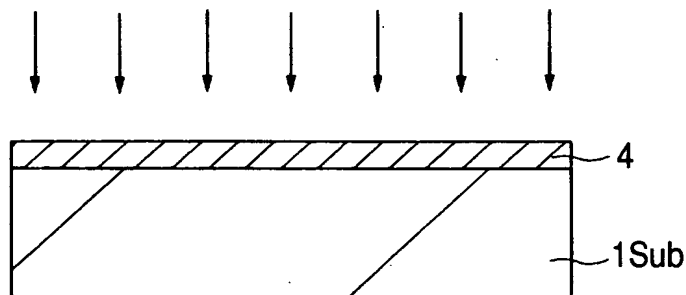
**FIG. 21**



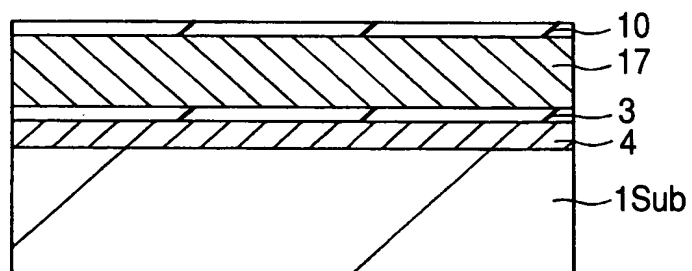
**FIG. 22**



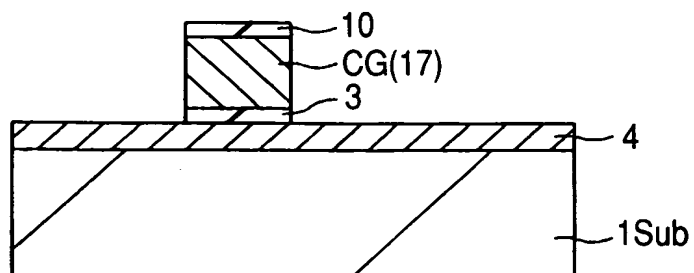
**FIG. 23**



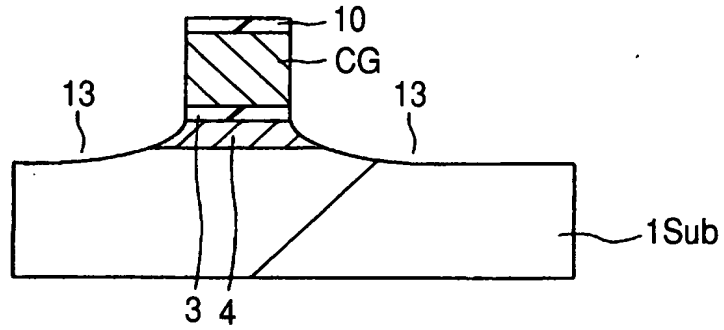
**FIG. 24**



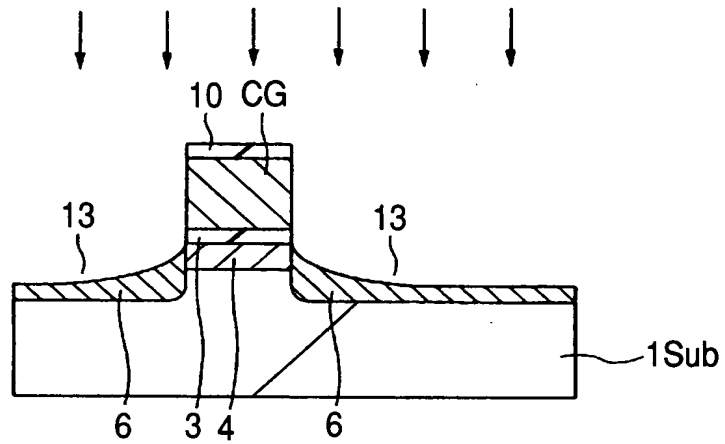
**FIG. 25**



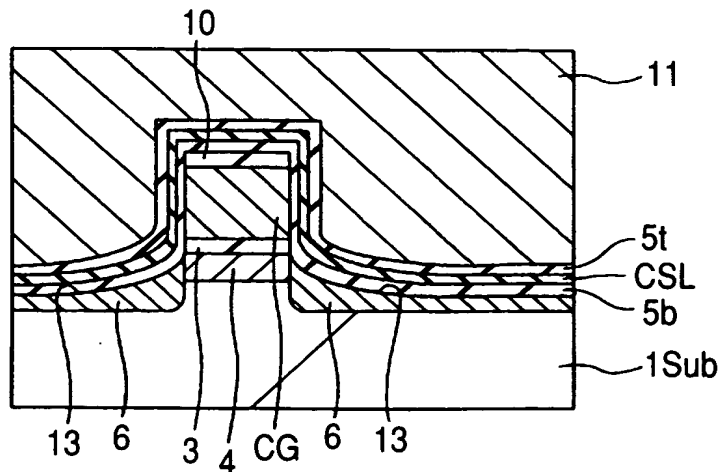
**FIG. 26**



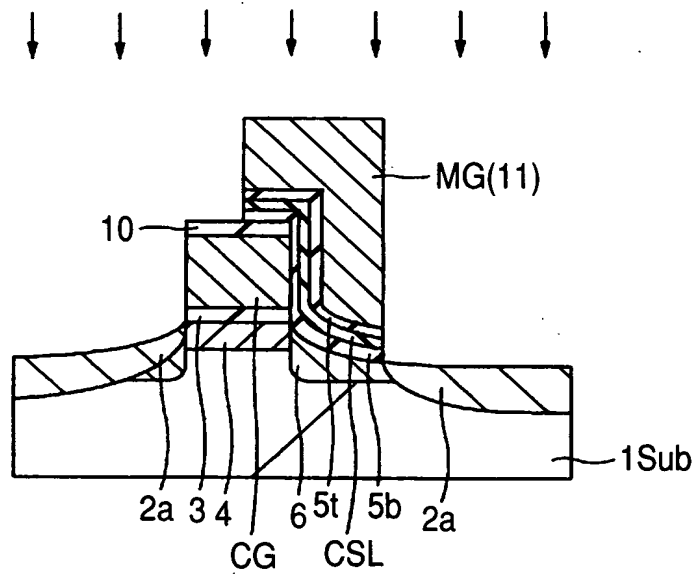
**FIG. 27**



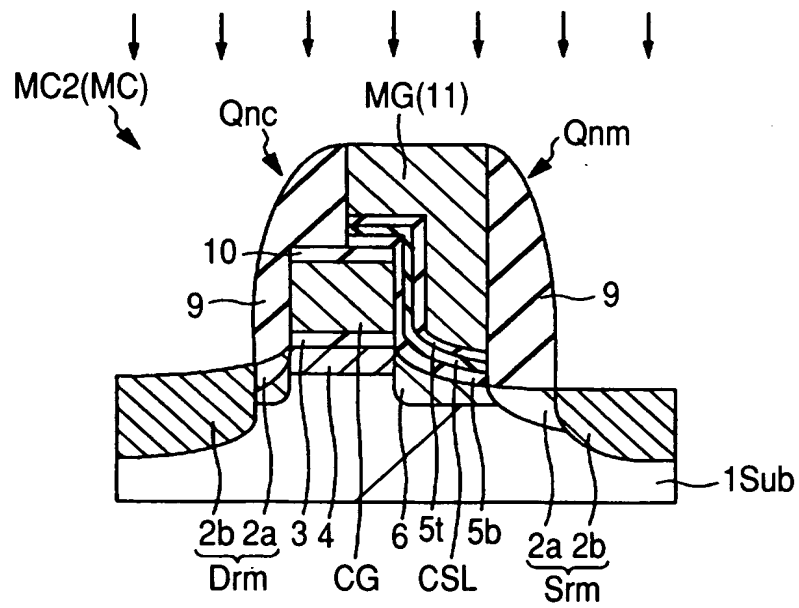
**FIG. 28**



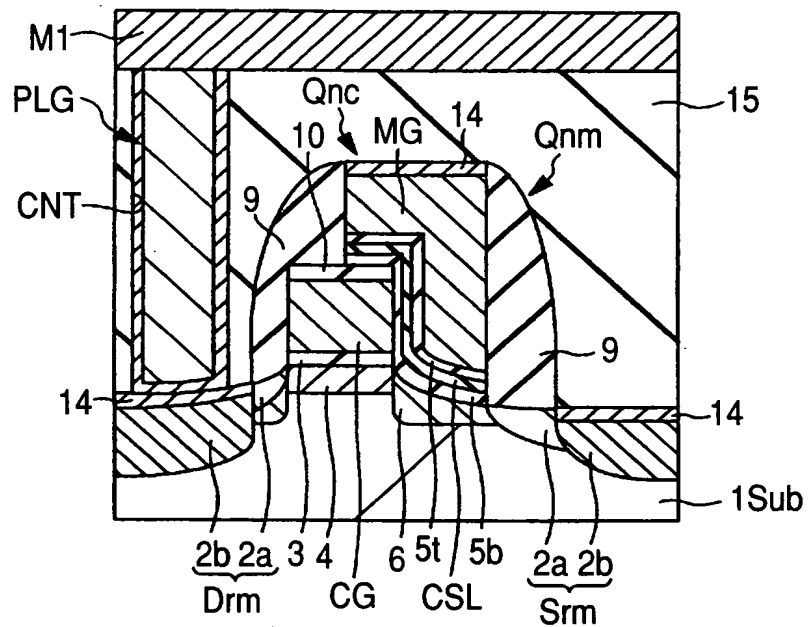
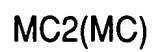
**FIG. 29**



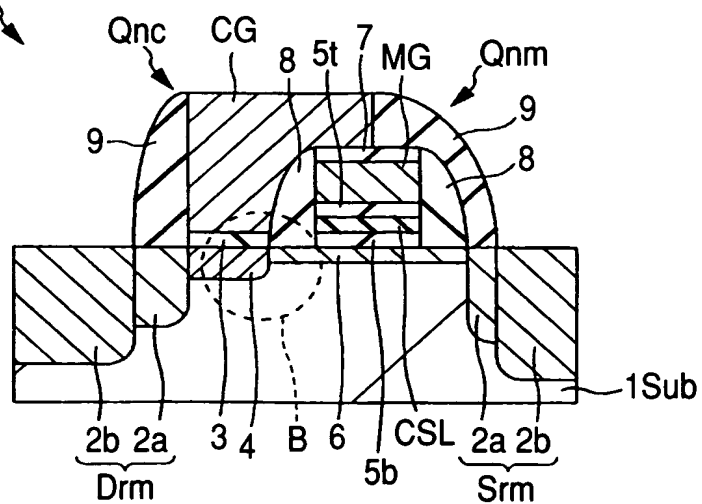
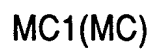
**FIG. 30**

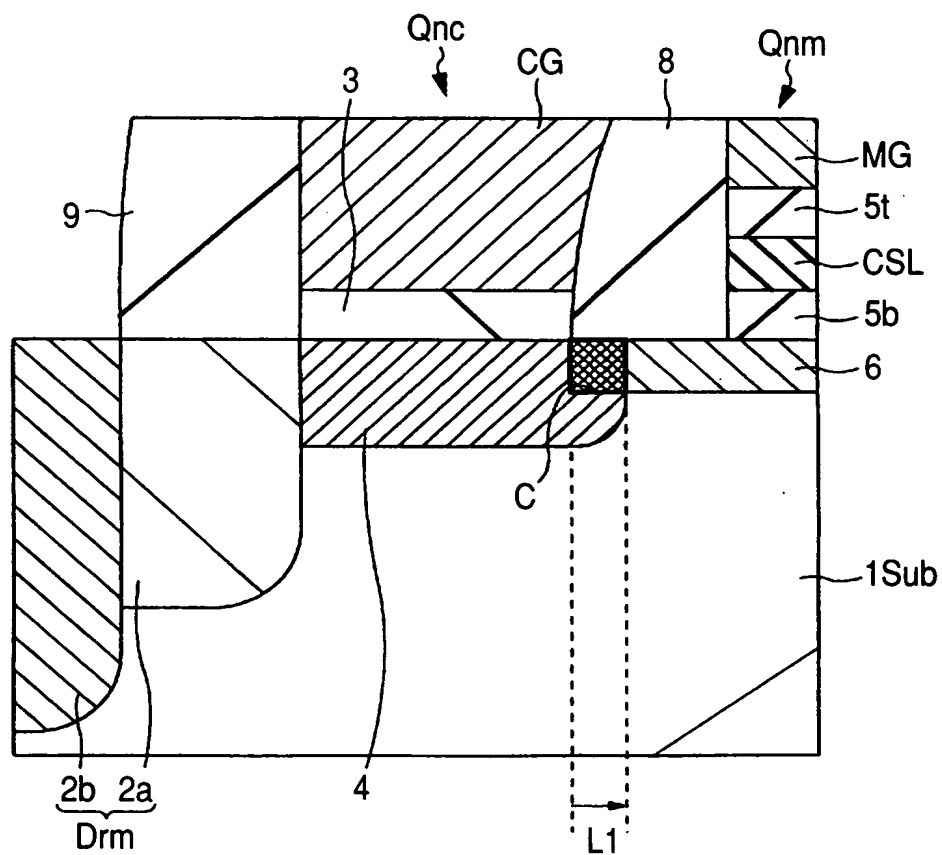


**FIG. 31**



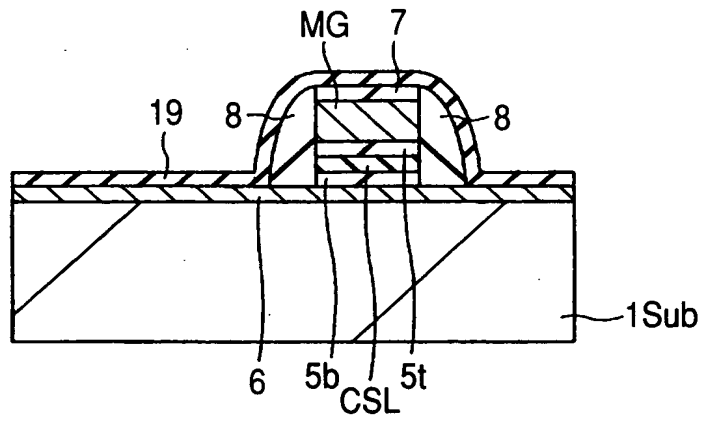
**FIG. 32**



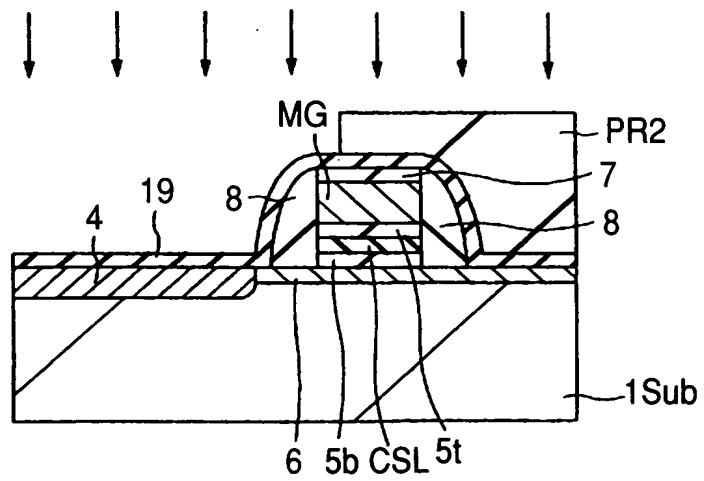
**FIG. 33**



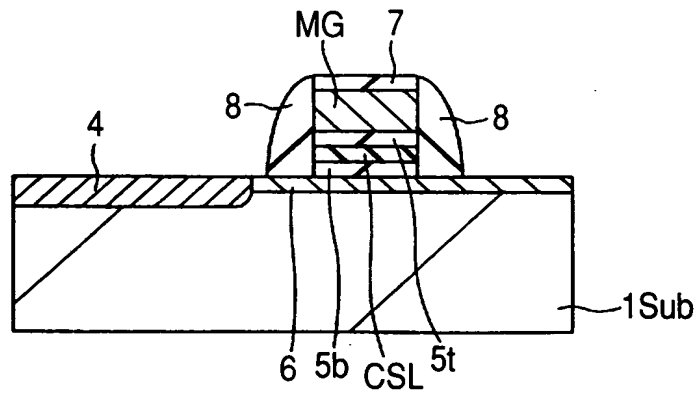
**FIG. 34**



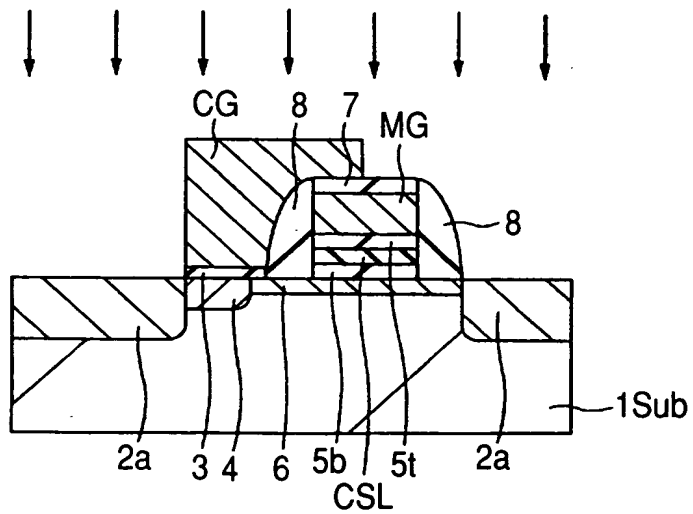
**FIG. 35**

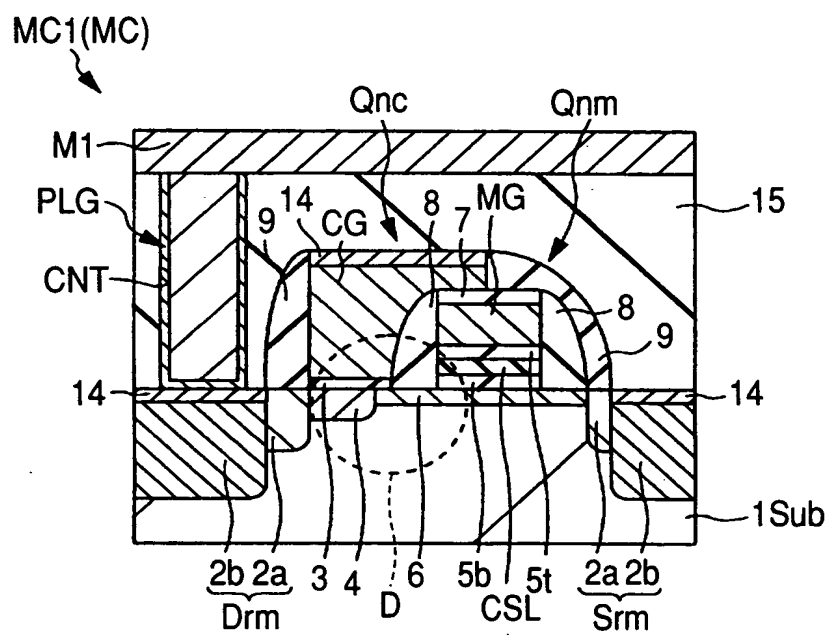


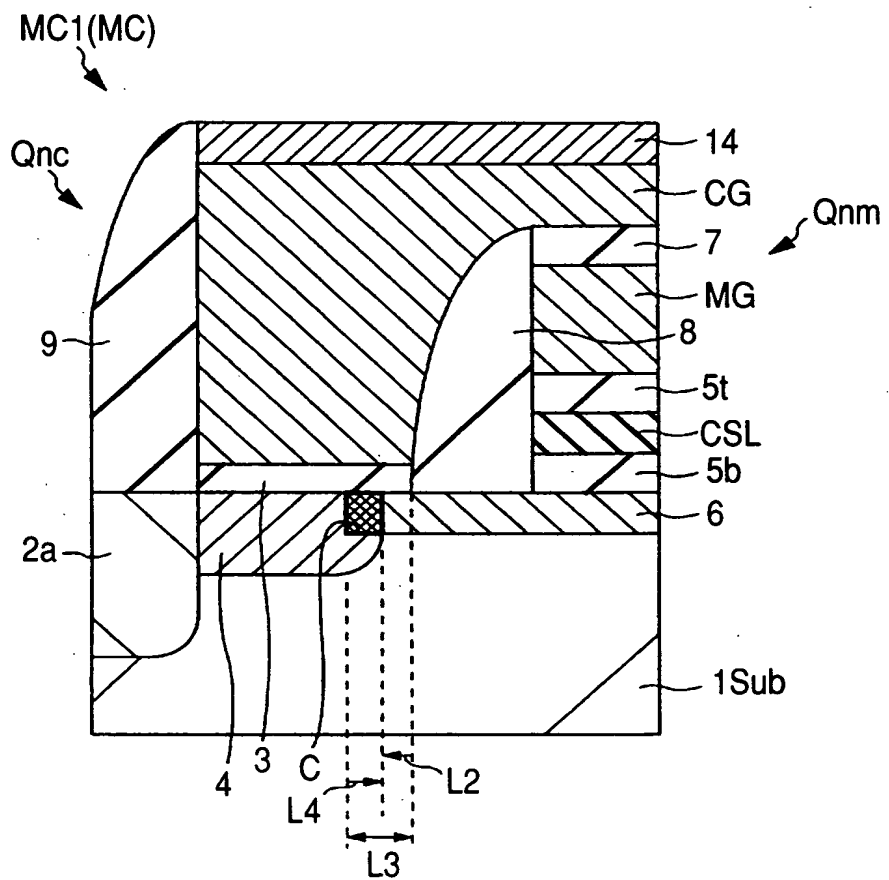
**FIG. 36**



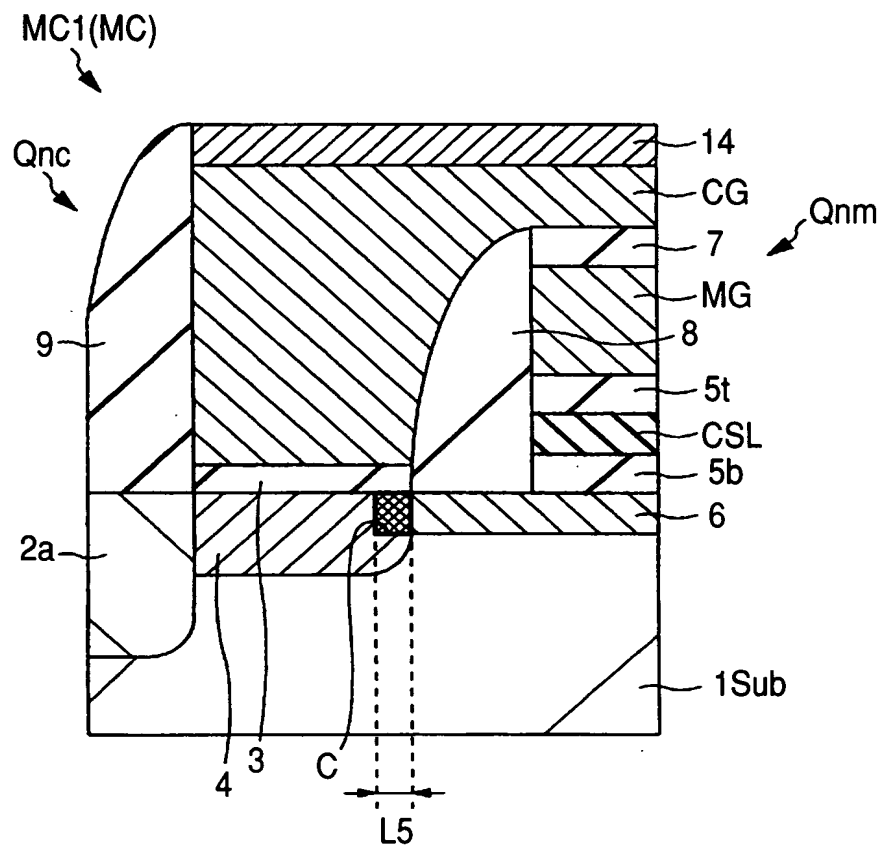
**FIG. 37**

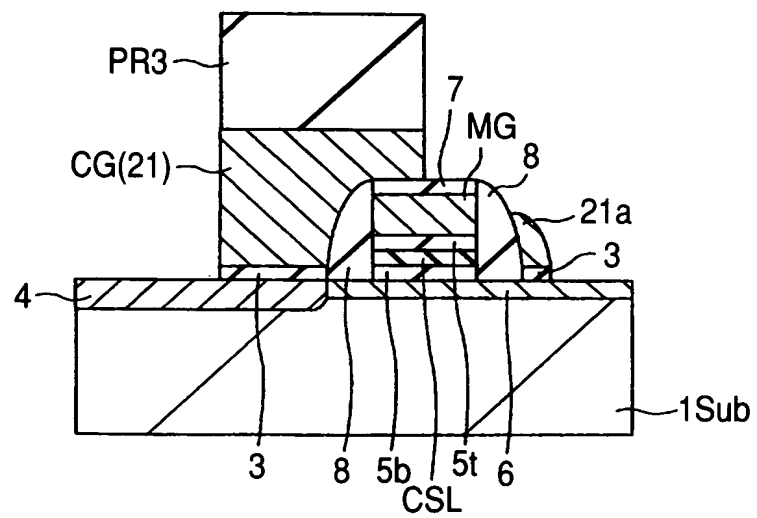




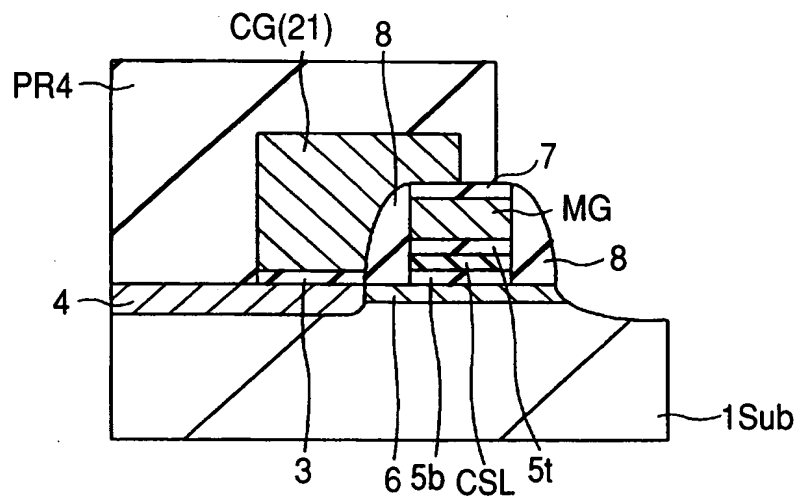
**FIG. 39**

**FIG. 40**

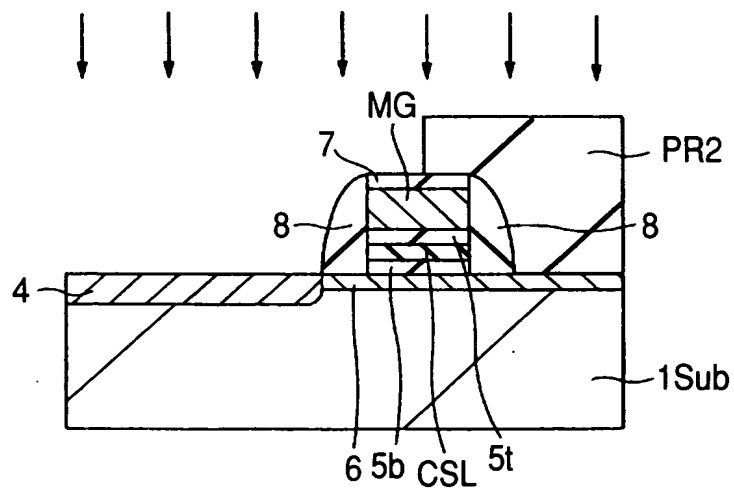




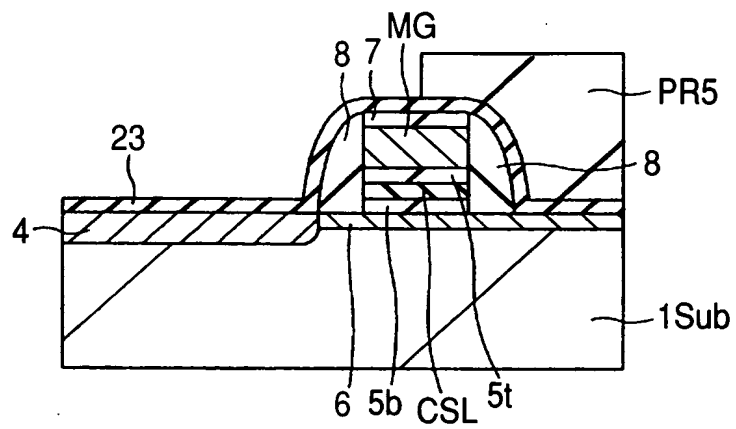
**FIG. 43**



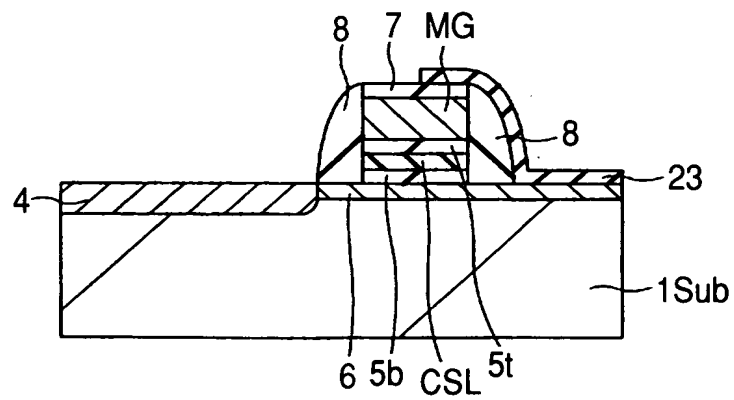
**FIG. 44**



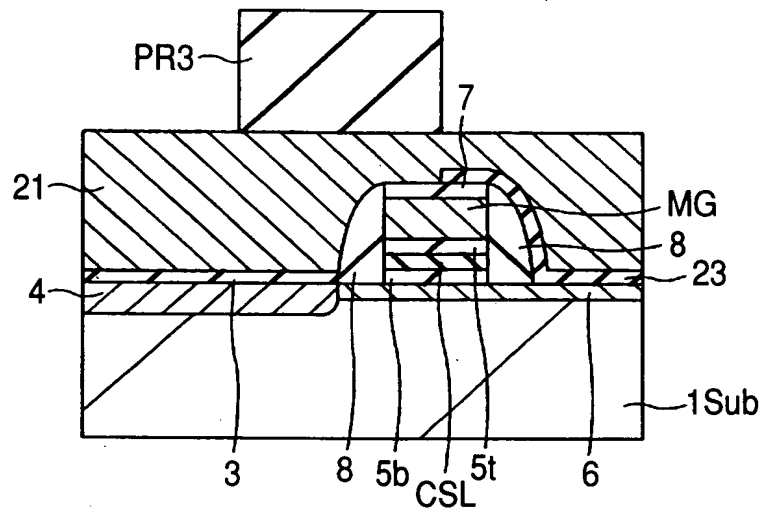
**FIG. 45**



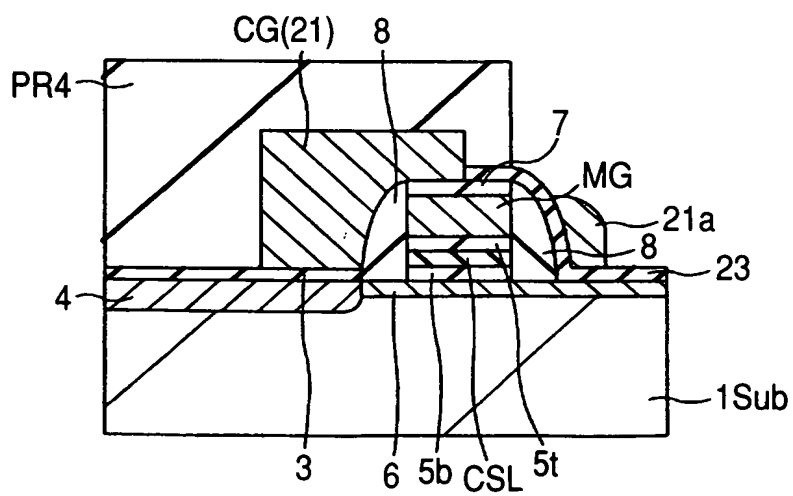
**FIG. 46**



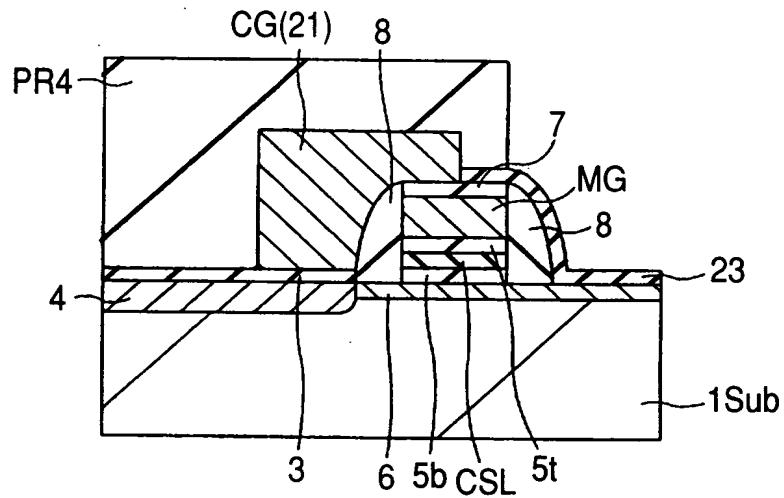




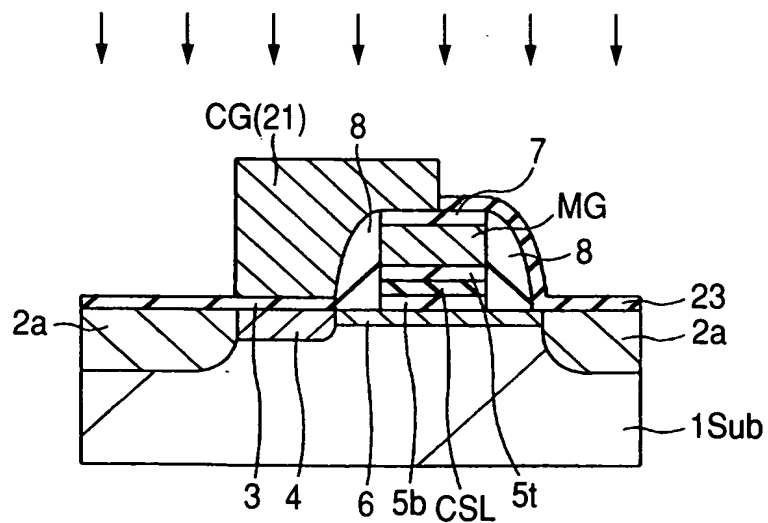
**FIG. 48**



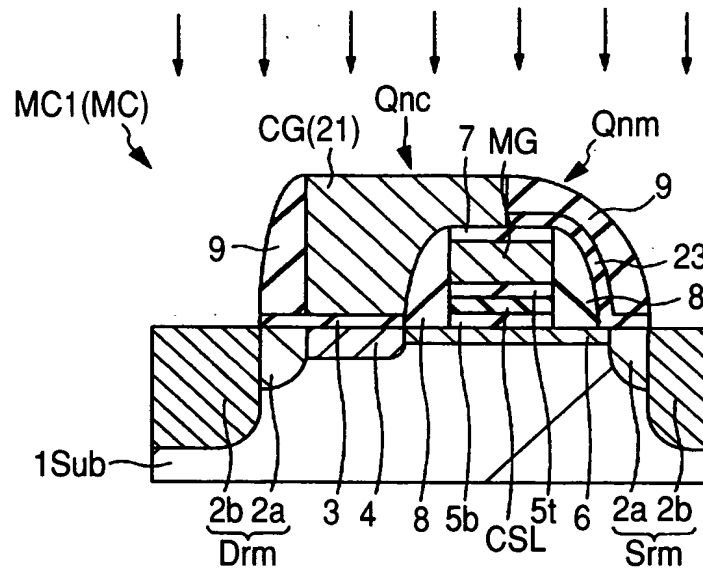
**FIG. 49**



**FIG. 50**



**FIG. 51**



**FIG. 52**

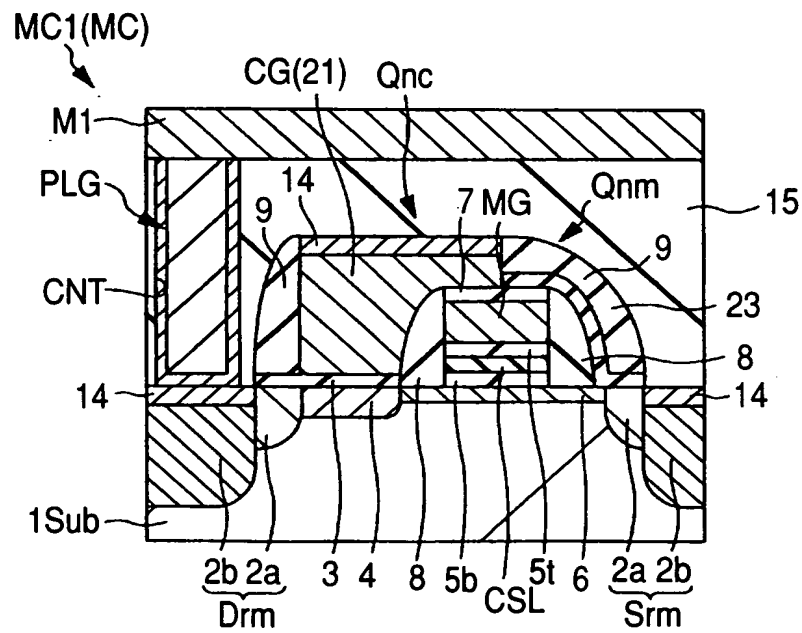


FIG. 53

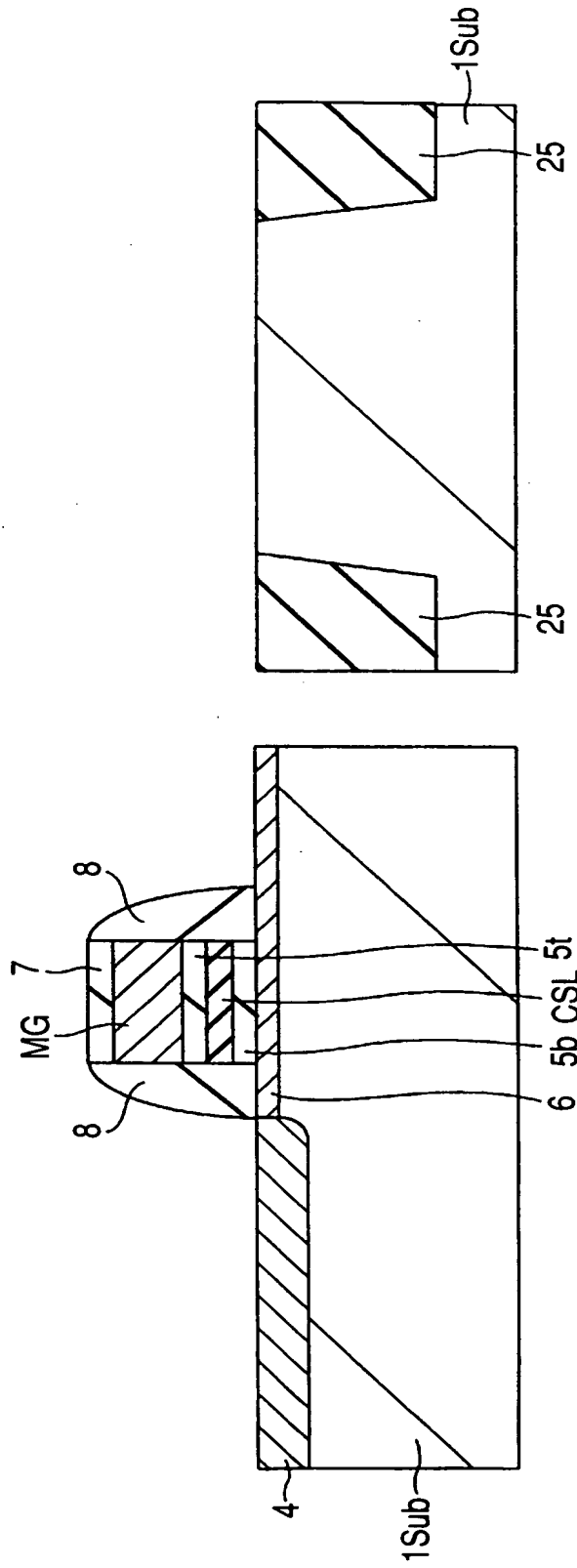


FIG. 54

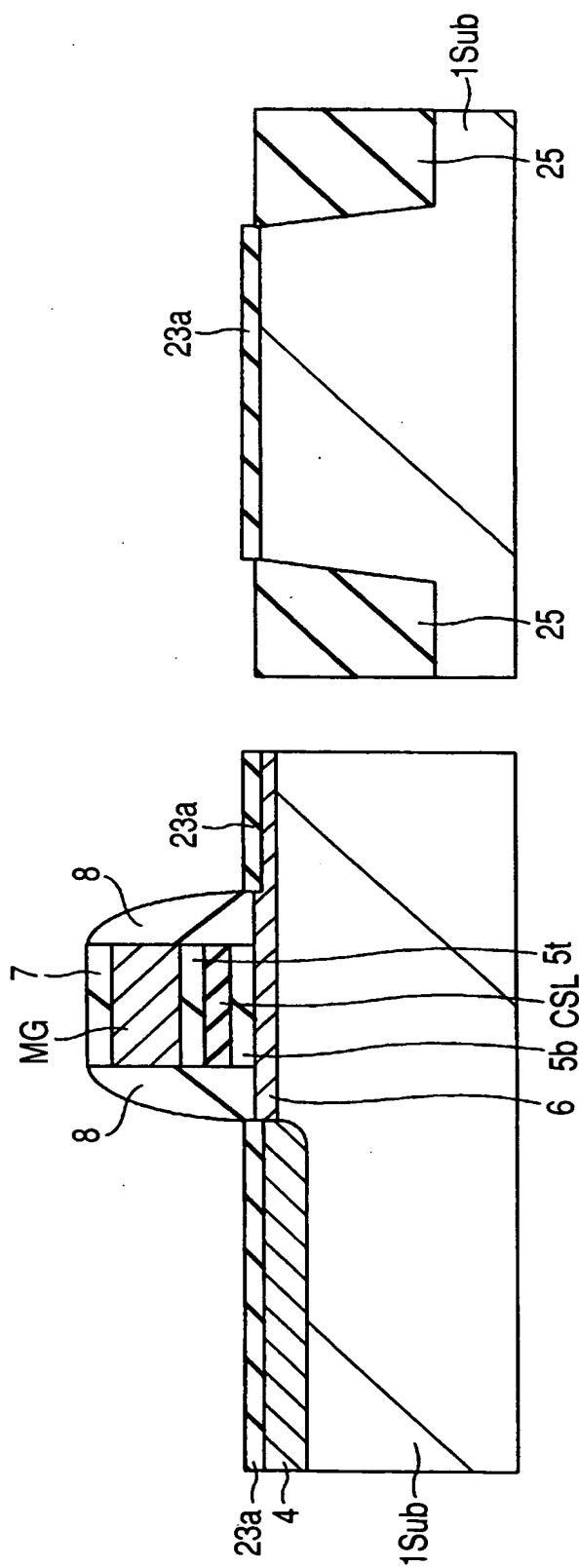


FIG. 55

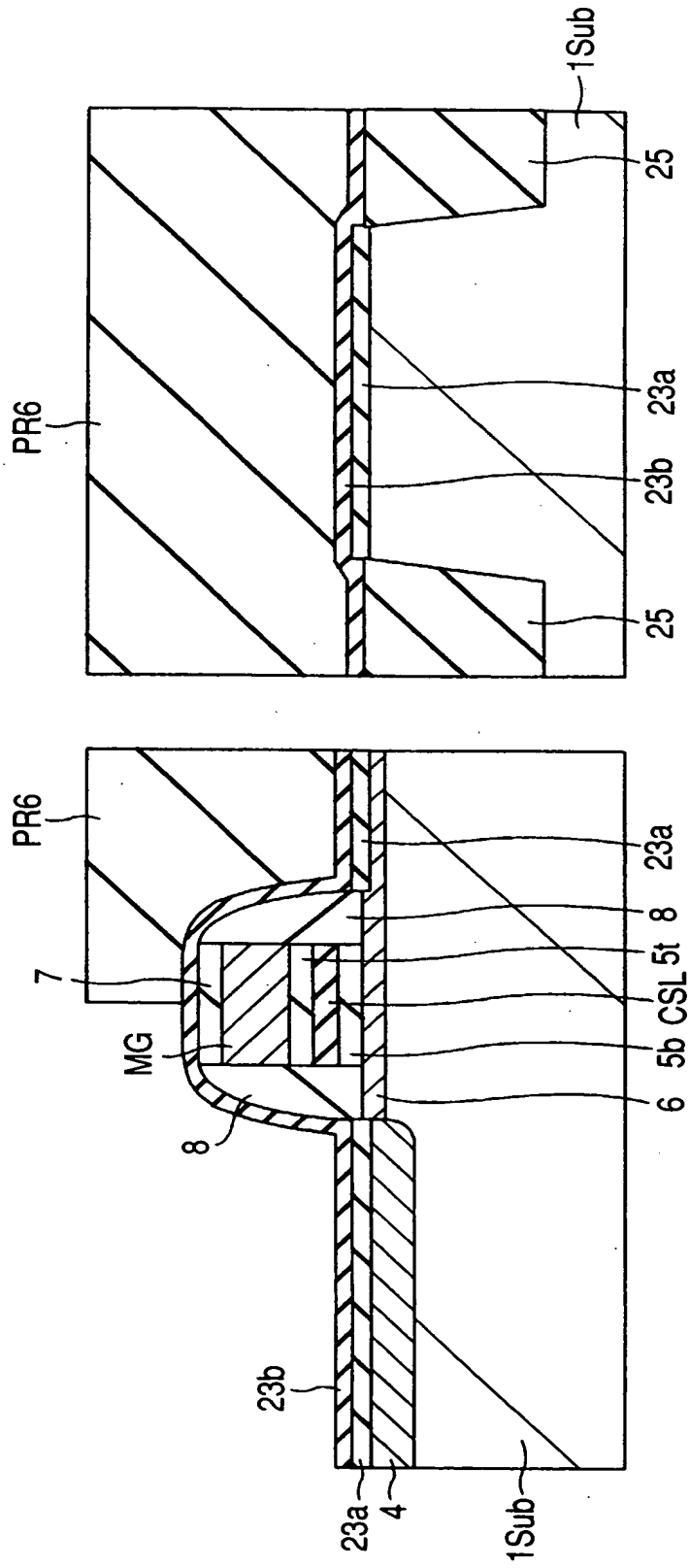


FIG. 56

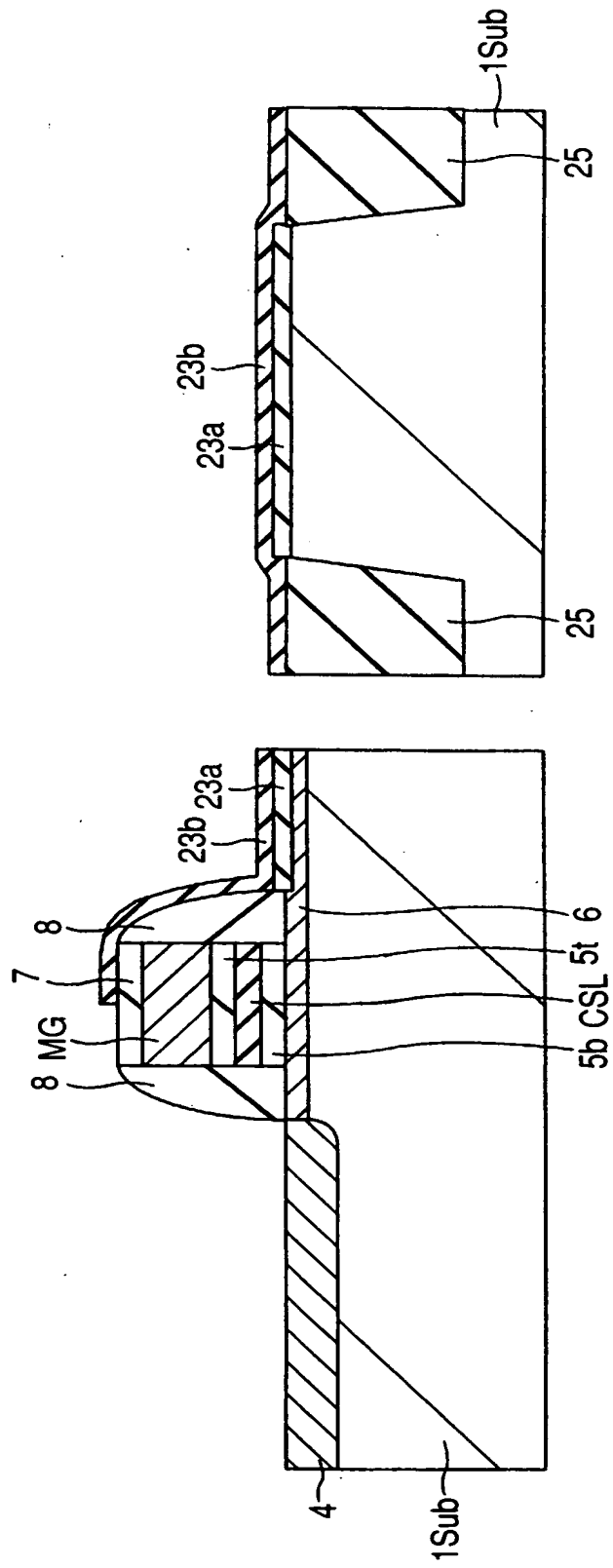


FIG. 57

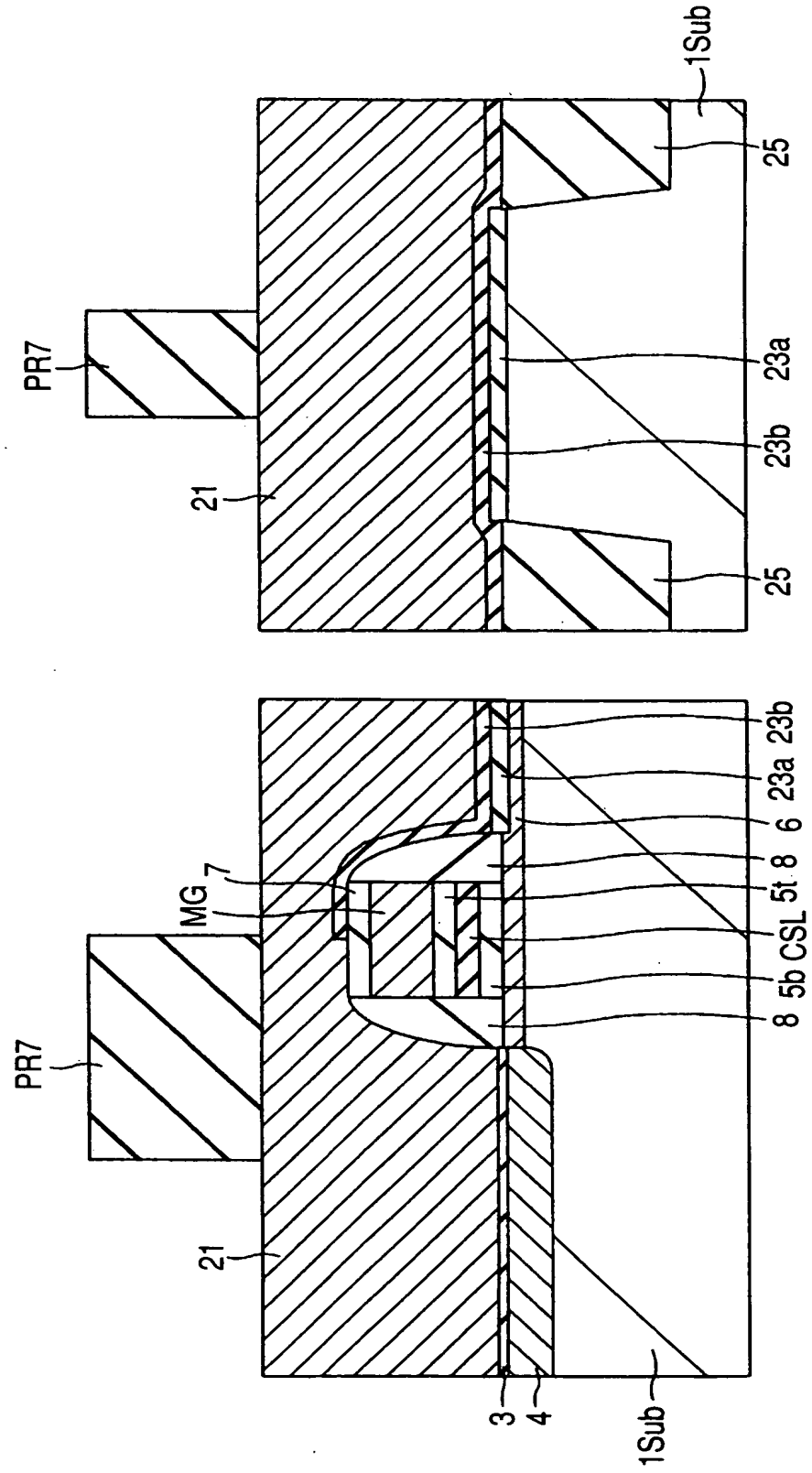




FIG. 58

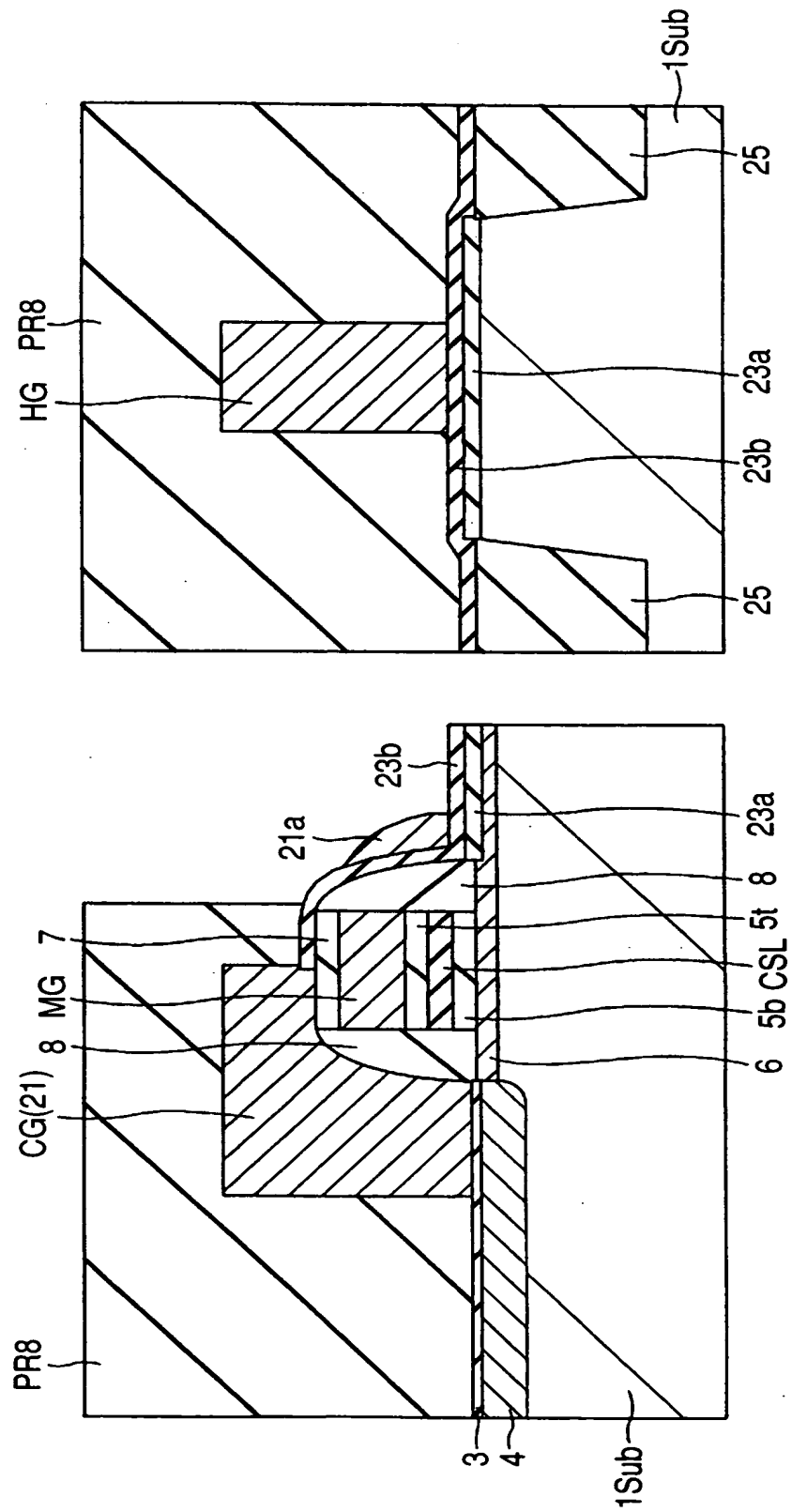
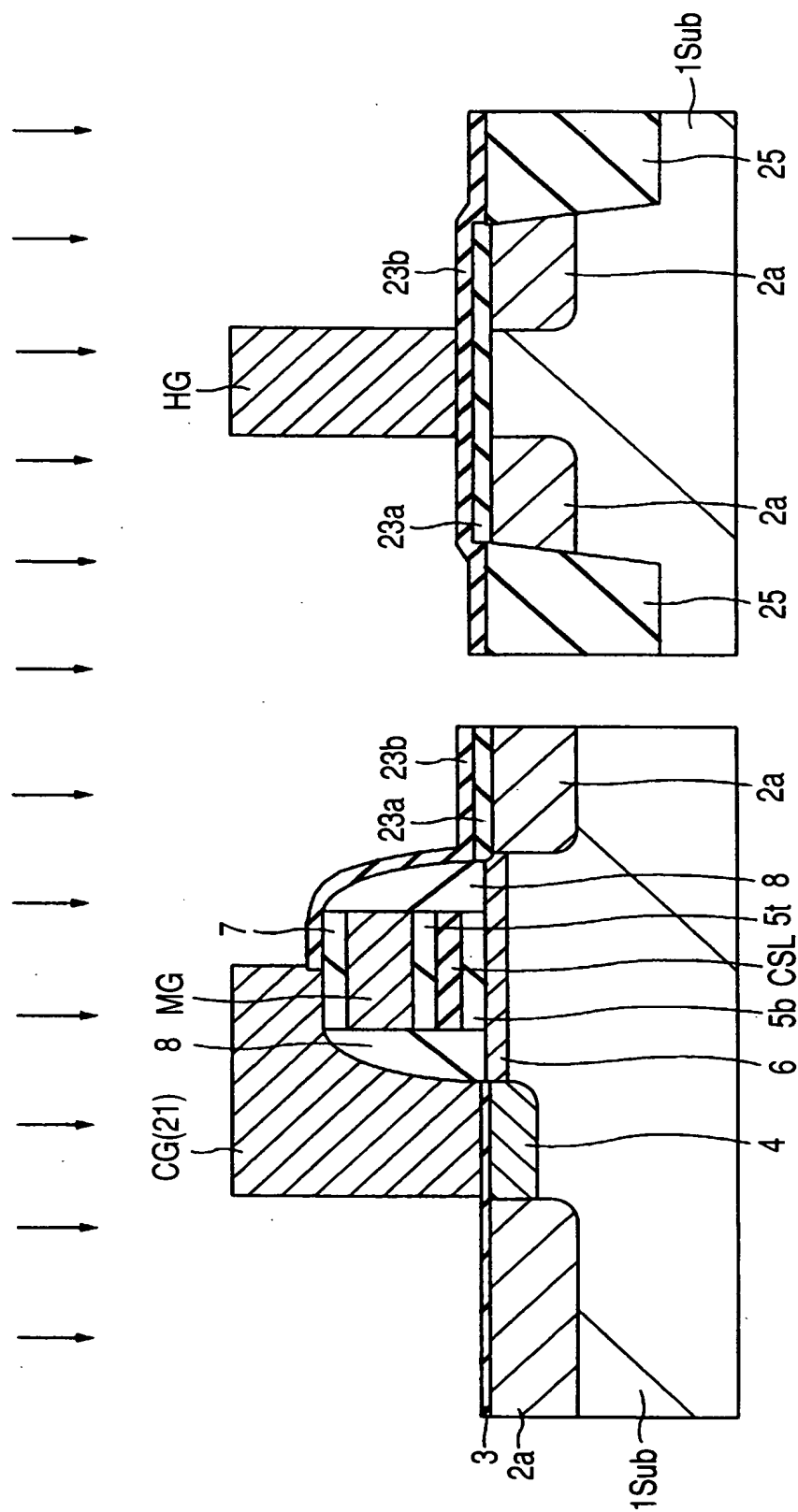
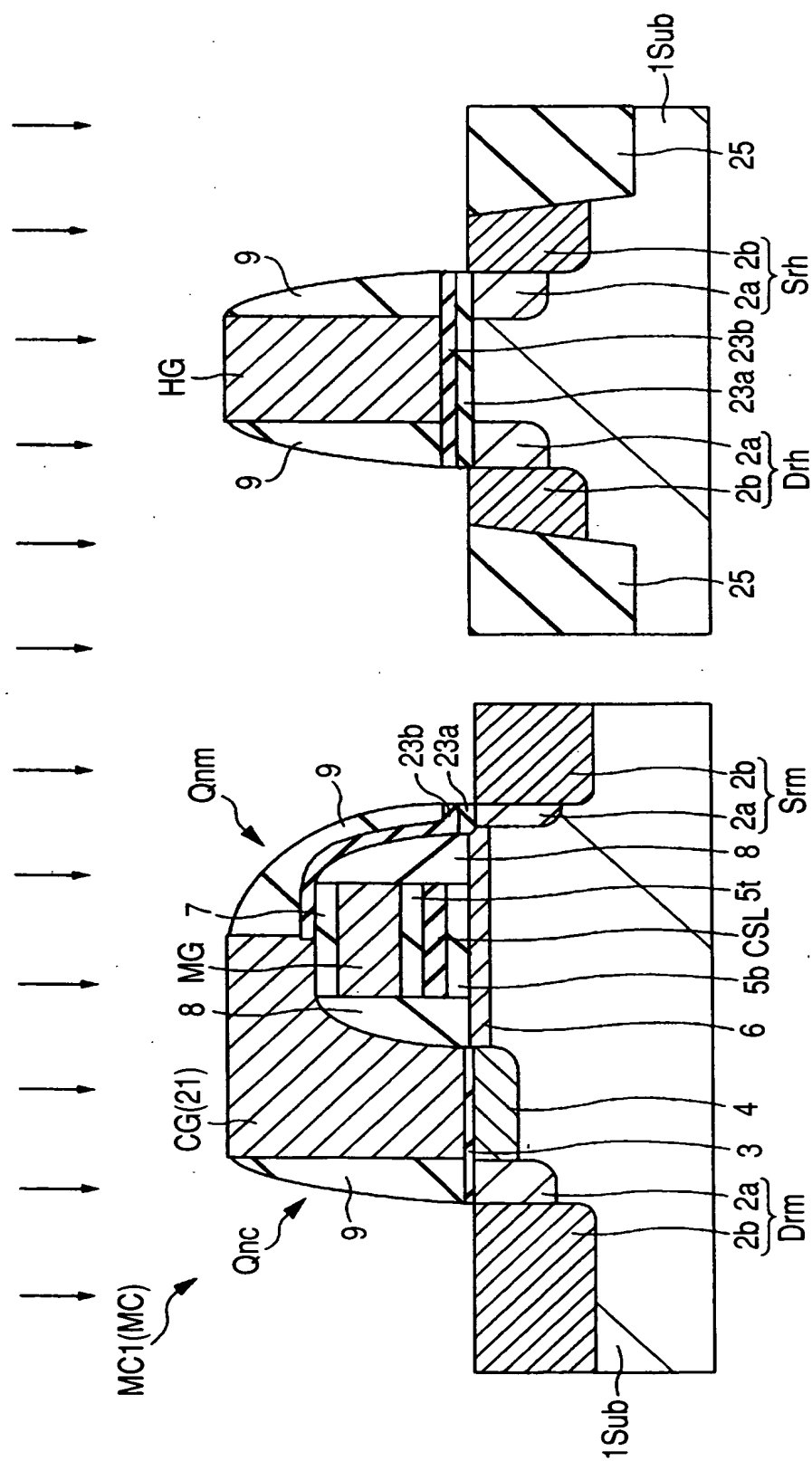


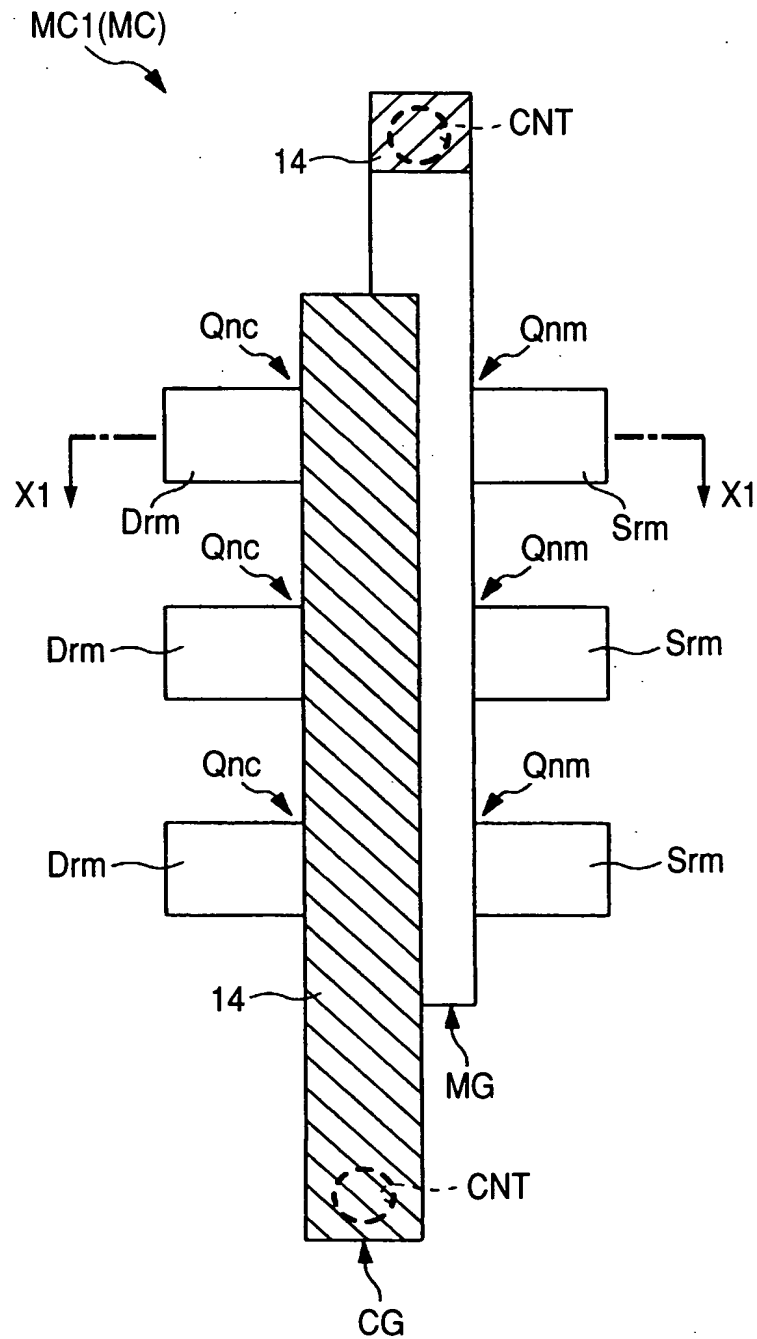


FIG. 60

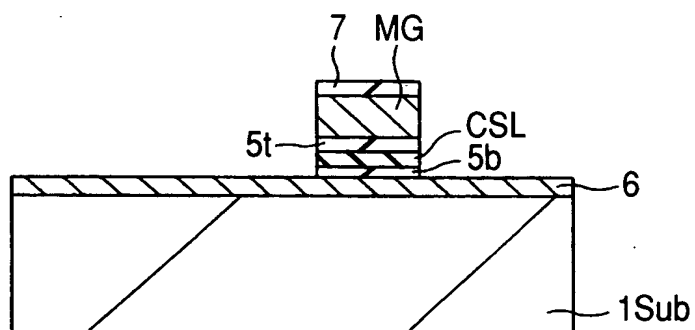


**FIG. 61**

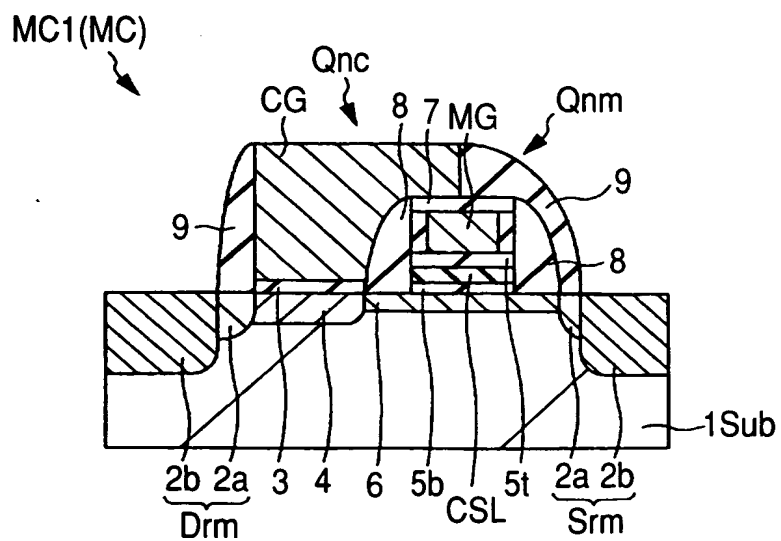


**FIG. 62**

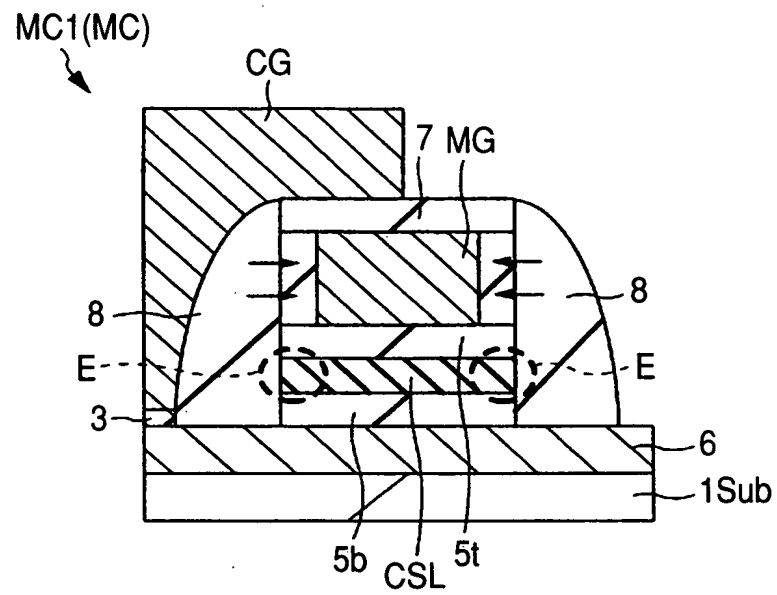
**FIG. 63**



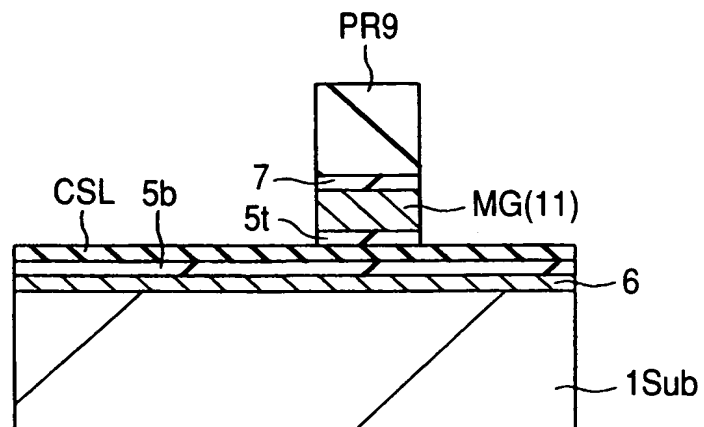
**FIG. 64**

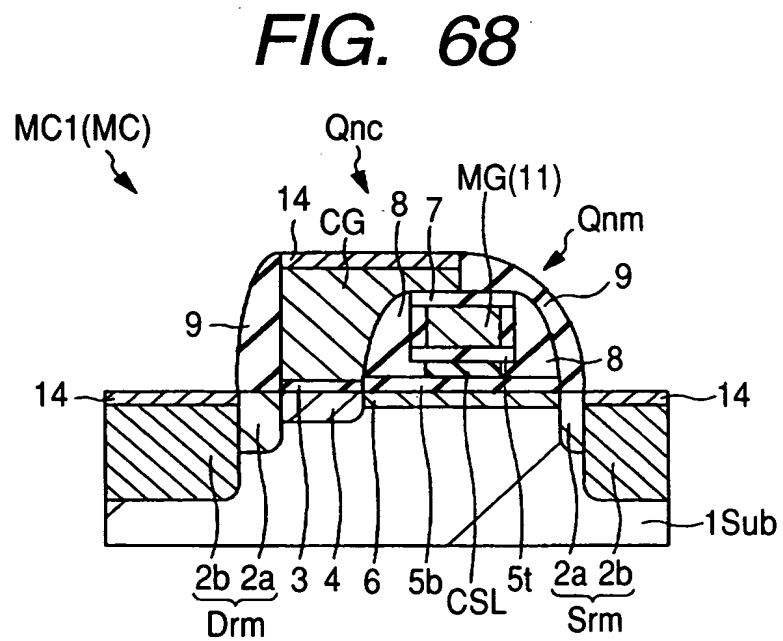


**FIG. 65**



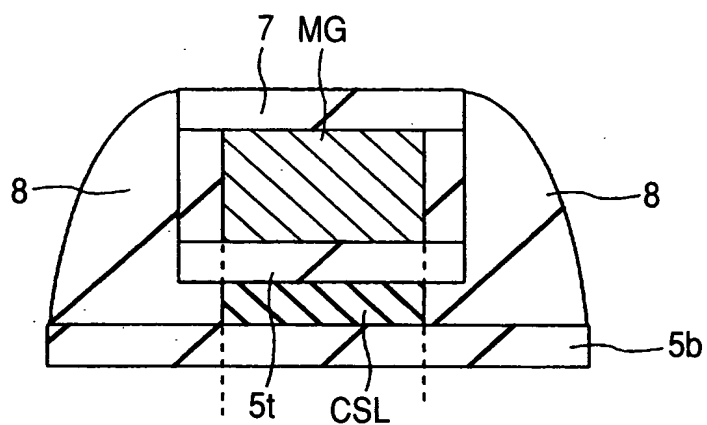
**FIG. 66**



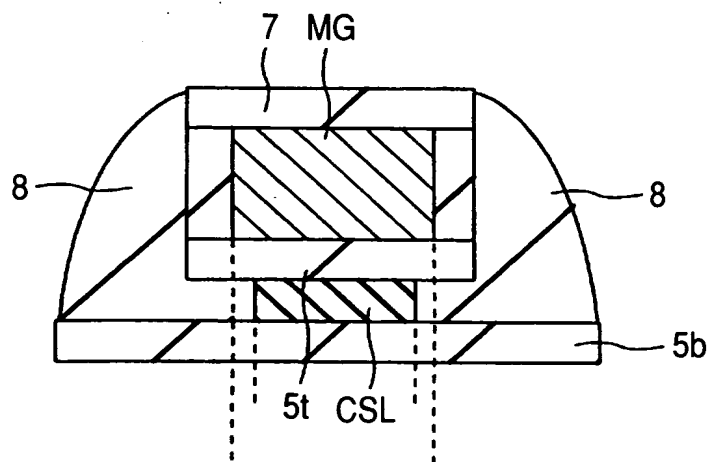


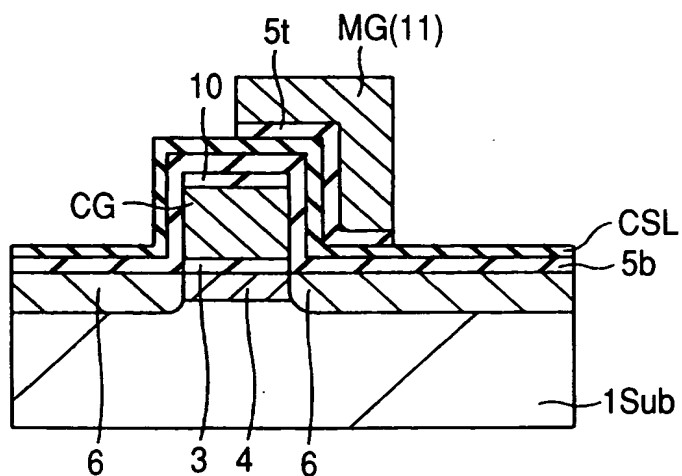


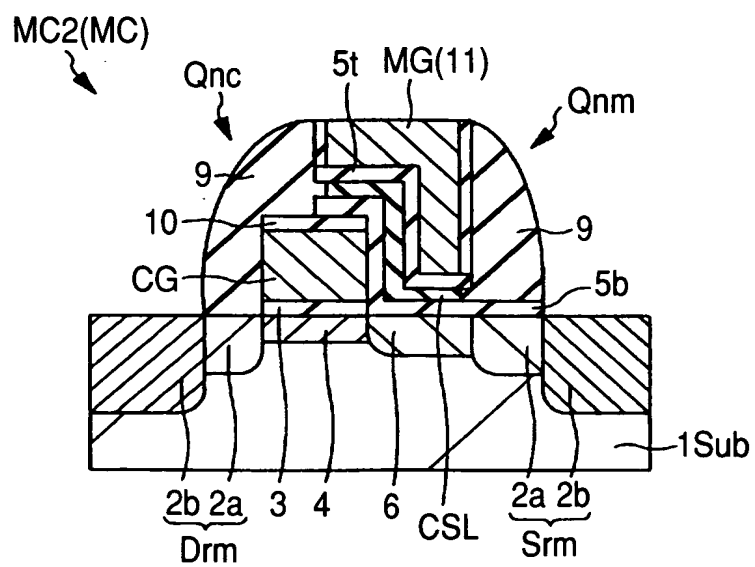
**FIG. 69**



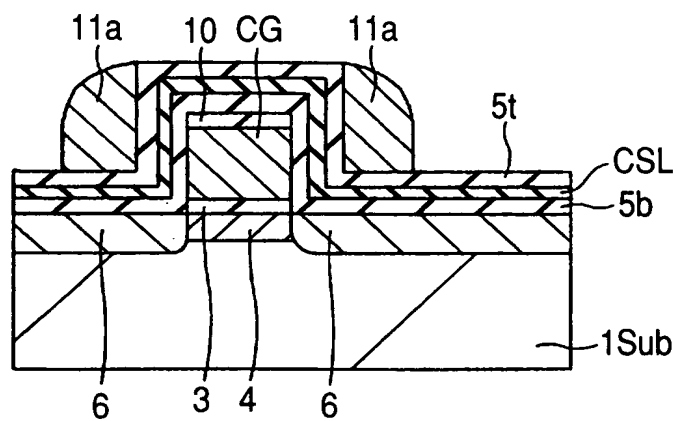
**FIG. 70**



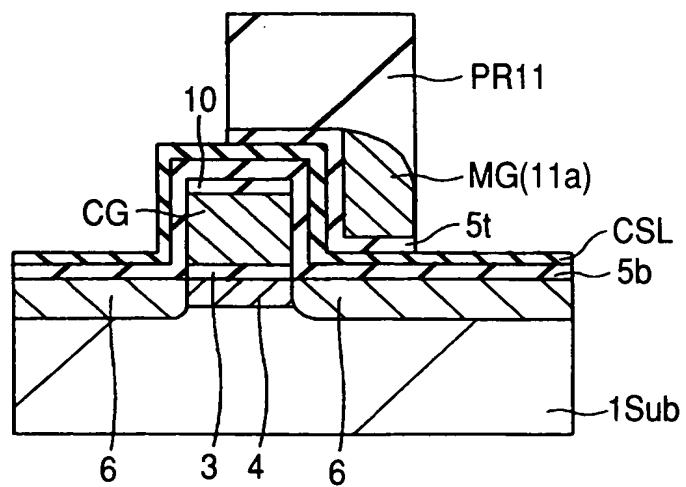


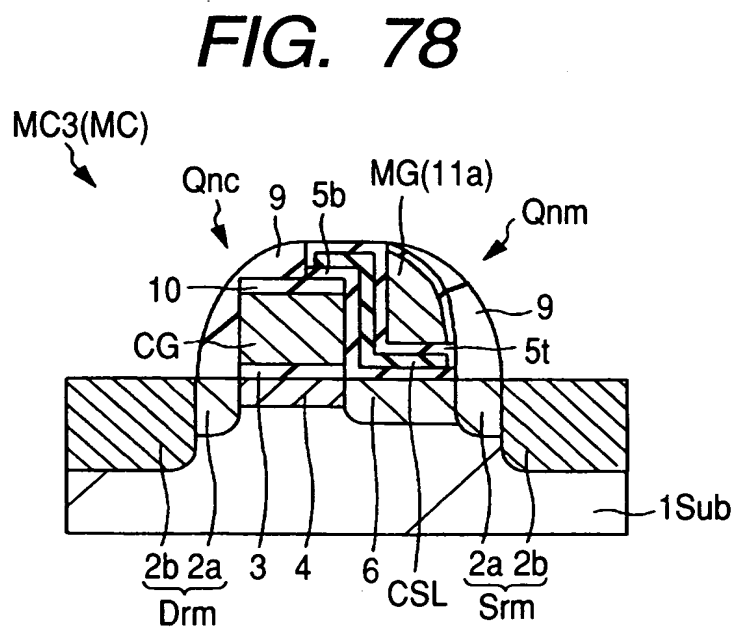


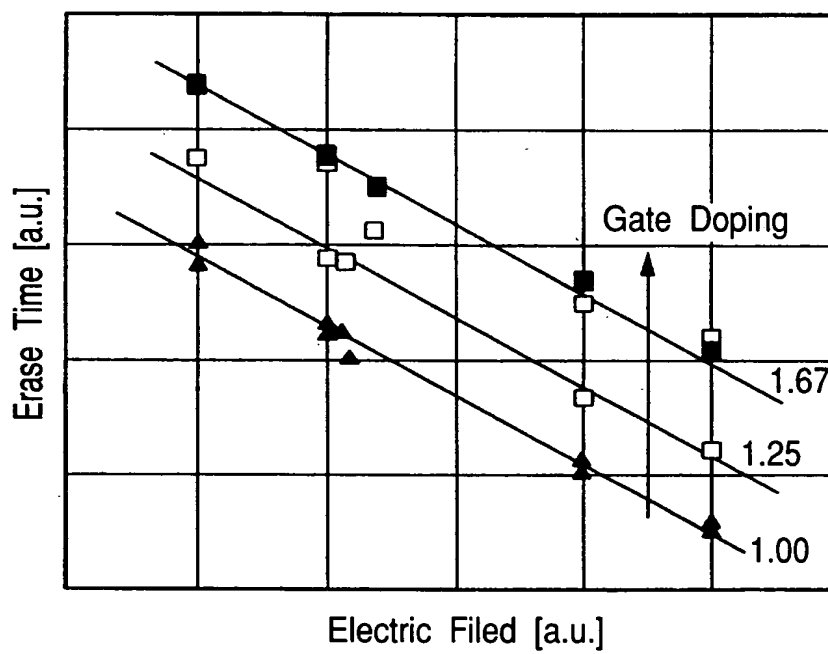
**FIG. 75**

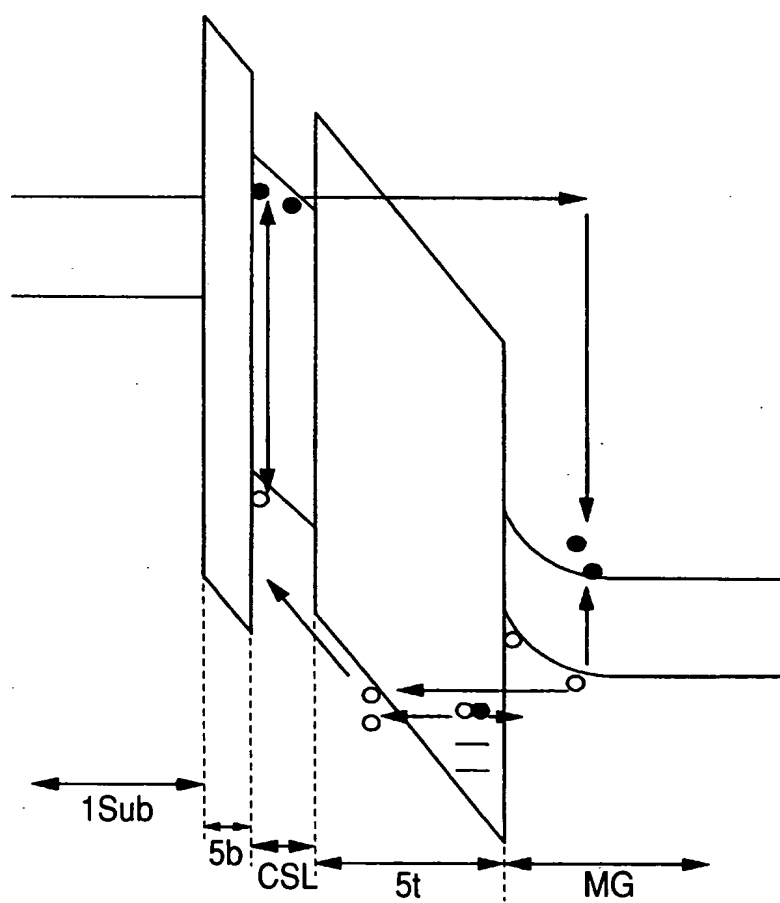


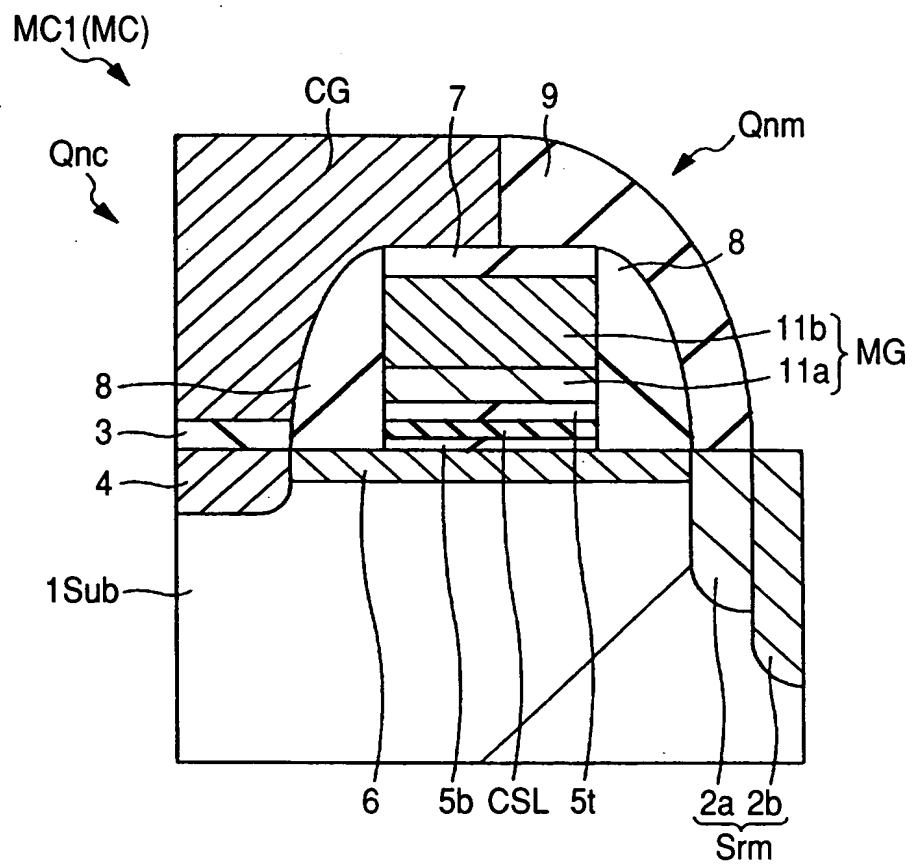
**FIG. 76**





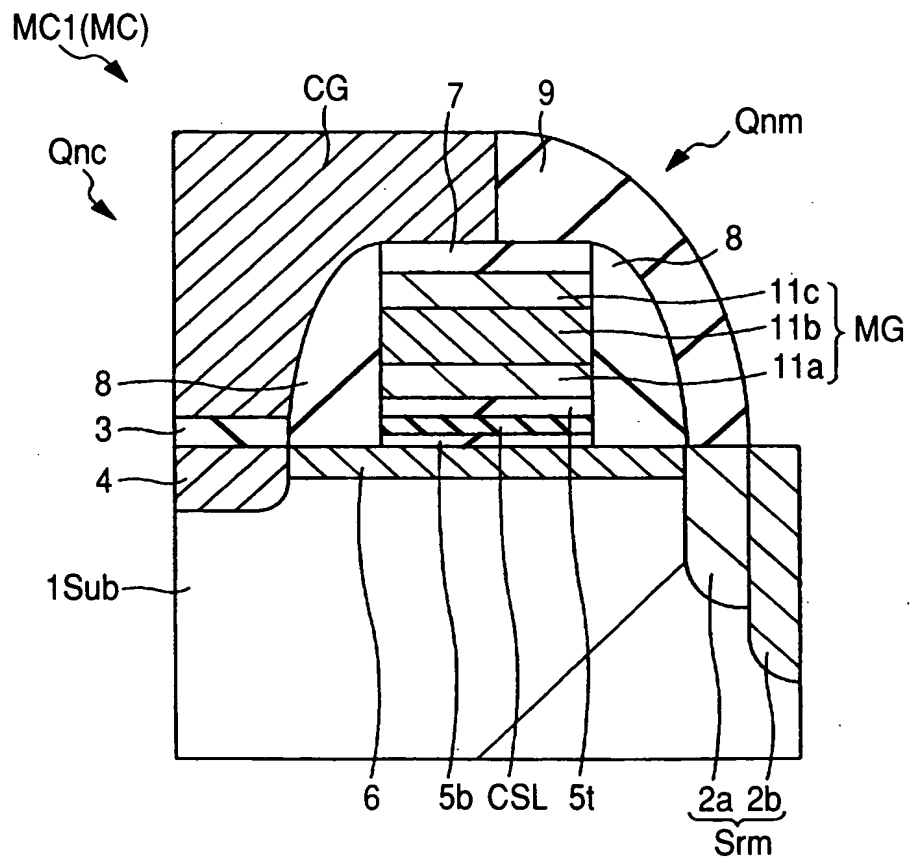
*FIG. 79*

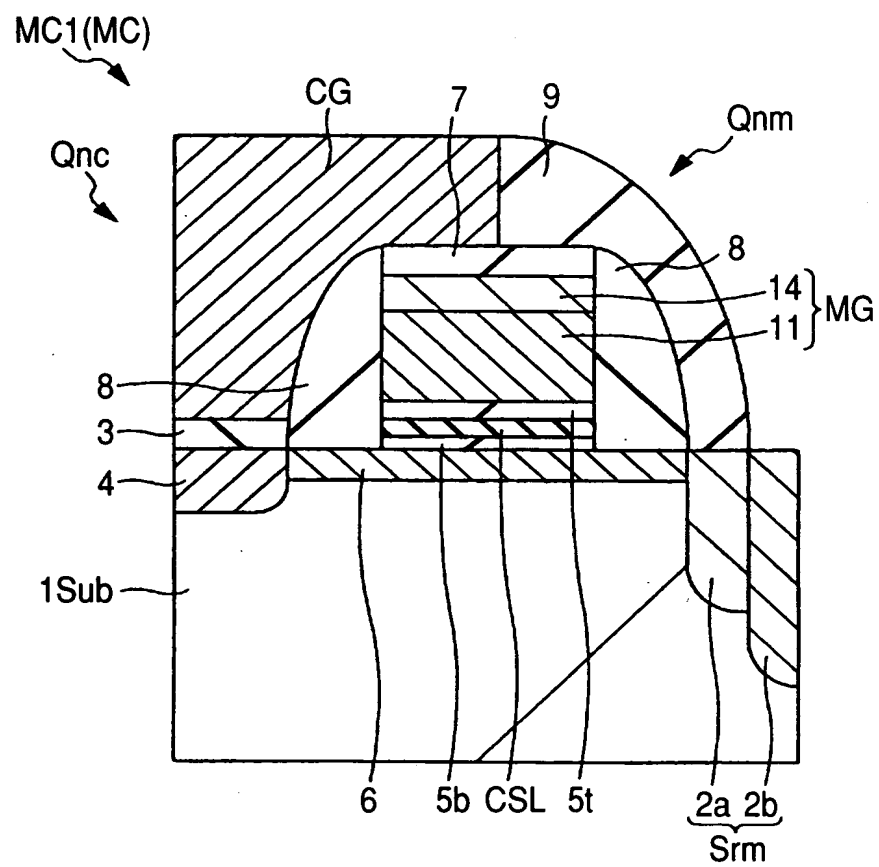
**FIG. 80**





**FIG. 82**







**FIG. 85**